



———— CIVIL ————
INFRASTRUCTURE
———— PLATFORM ————

Upstream First is Our Principle – Toward Super Long-Term Support –

Chris Paterson @ Renesas

Masashi Kudo @ Cybertrust Japan

October 26th , 2020



- **Masashi Kudo** <masashi.kudo@miraclelinux.com>
 - Working for Cybertrust Japan Co., Ltd.
 - Acted as OpenDaylight (LF Networking) Ambassador
 - CIP Kernel Team Chair
- **Chris Paterson** <chris.paterson2@renesas.com>
 - Working for Renesas Electronics Europe GmbH
 - CIP Testing Working Group Chair





What is CIP ?

Upstream First

CIP Kernel Team Activities

CIP Automated Testing

Summary

What is CIP?



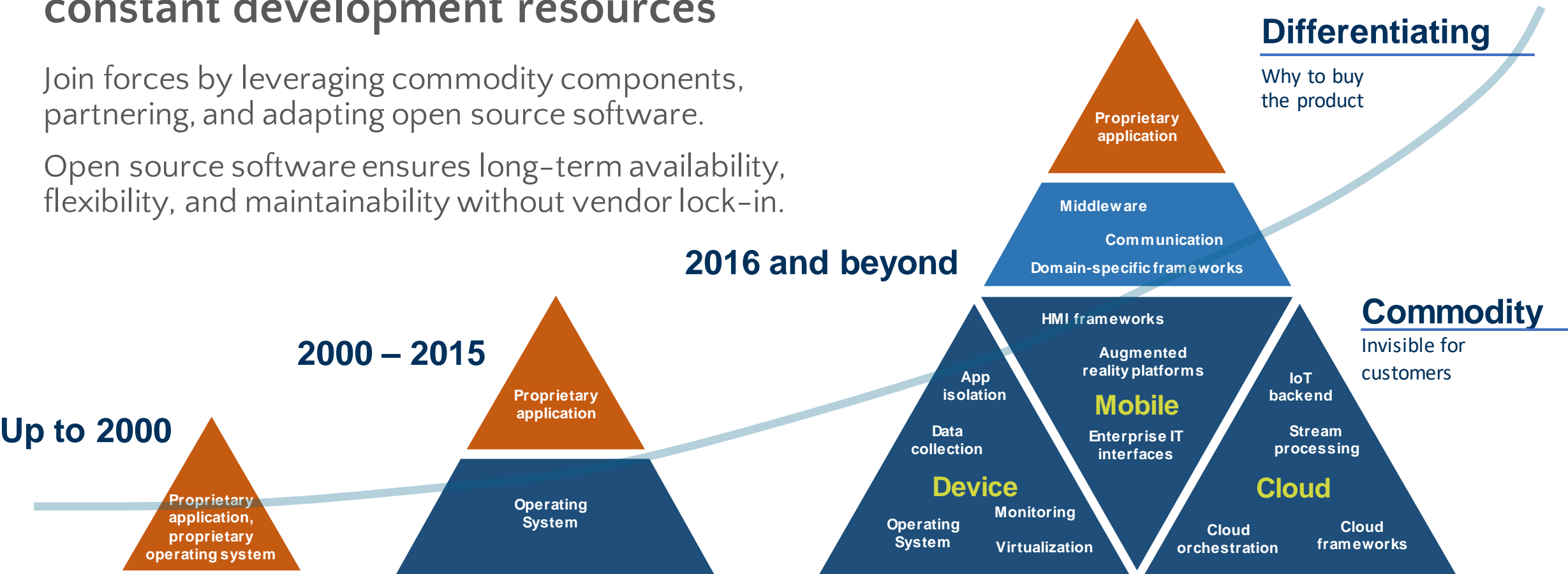
Speed and efficiency: focus on differentiating parts



Handling increasing complexity with constant development resources

Join forces by leveraging commodity components, partnering, and adapting open source software.

Open source software ensures long-term availability, flexibility, and maintainability without vendor lock-in.



Facts and Issues: Silo Development

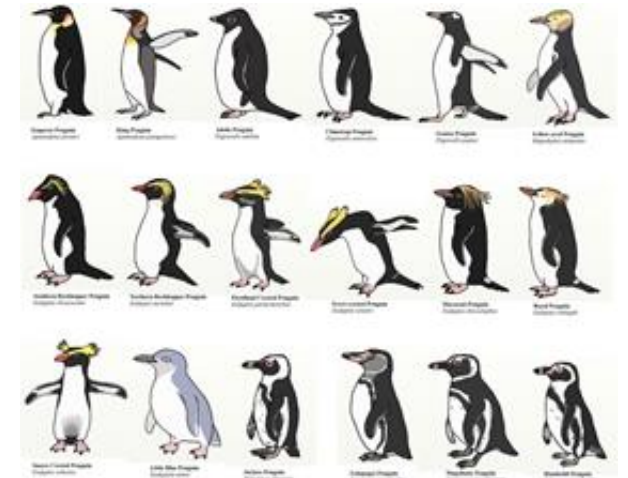
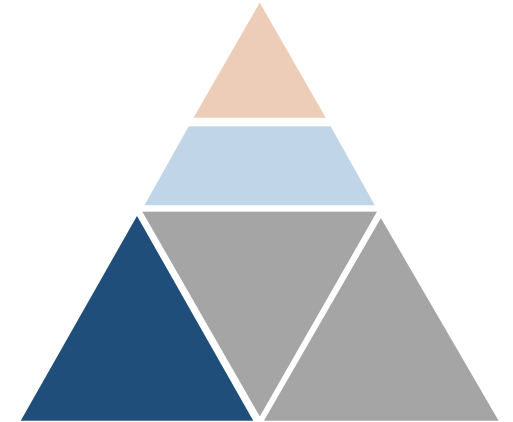


Facts

- Millions or trillions Industrial devices, including smart devices
- Similar software components (e.g. Linux)
- Industrial IoT requirements
 - Security
 - Sustainability
 - Industrial-gradeness

Issues

- A lot of products have to meet industrial requirements
- Same development and maintenance efforts spent by many companies or even business units
- **No common solution** for base building blocks



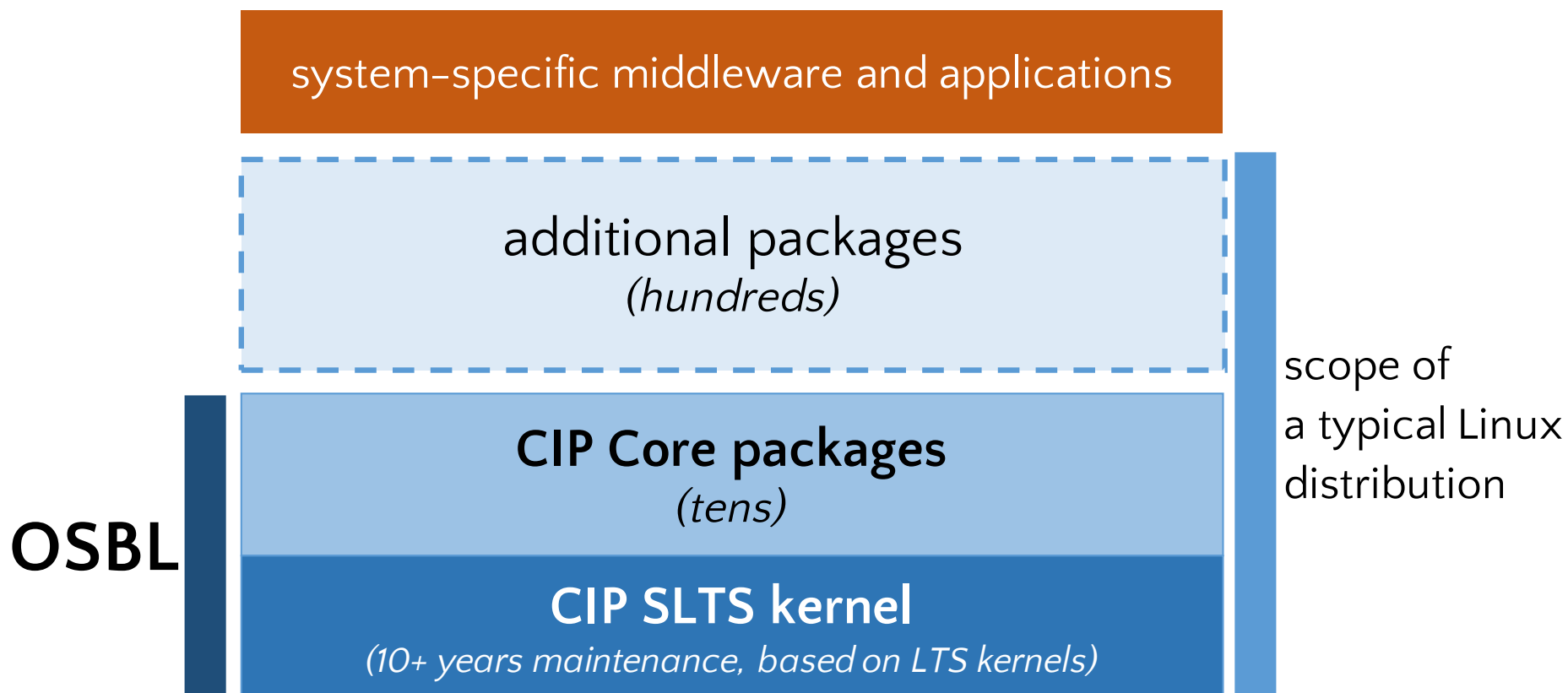
CIP is the Solution

Establishing an
Open Source Base Layer
of industrial-grade software to
enable the use and
implementation of software
building blocks for
Civil Infrastructure Systems



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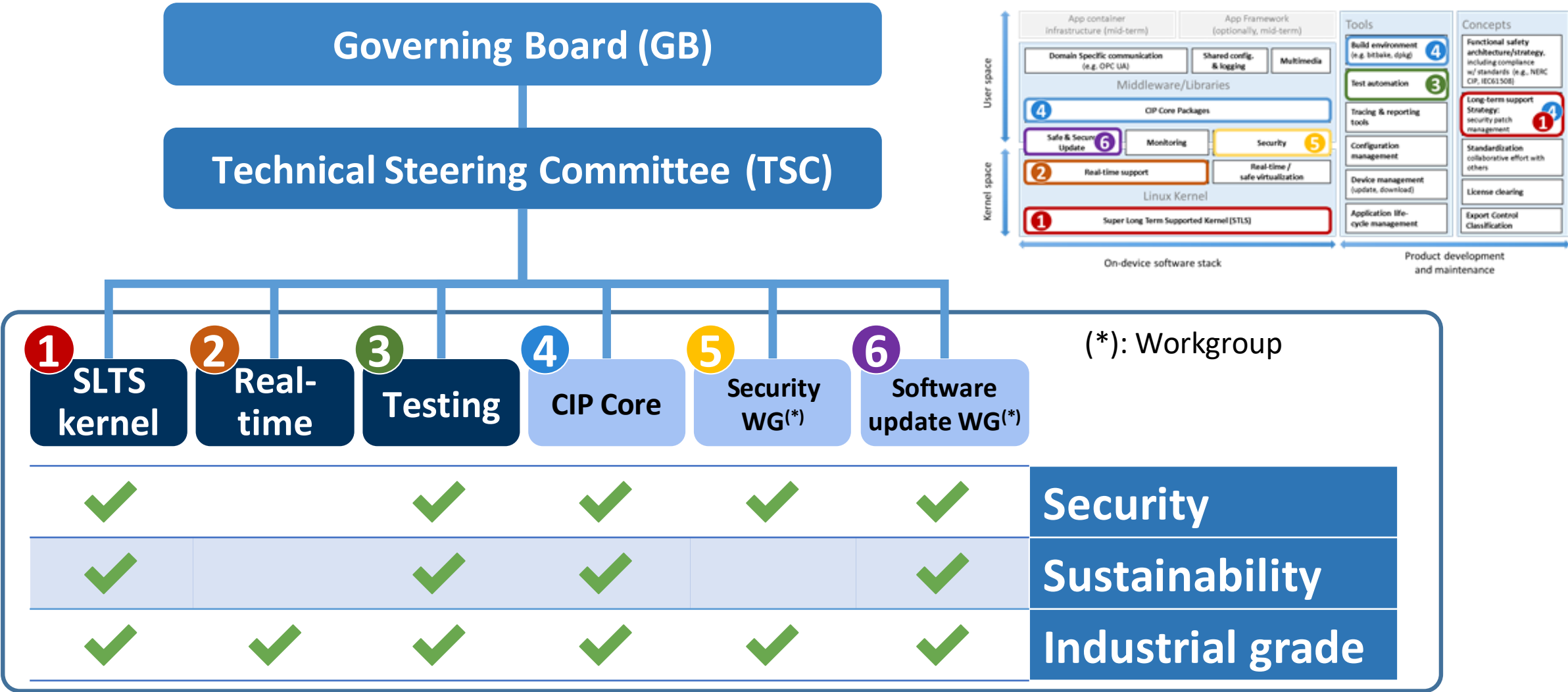
What is “Open Source Base Layer (OSBL)”?



CIP Civil Infrastructure Platform Project (<https://www.cip-project.org/>)

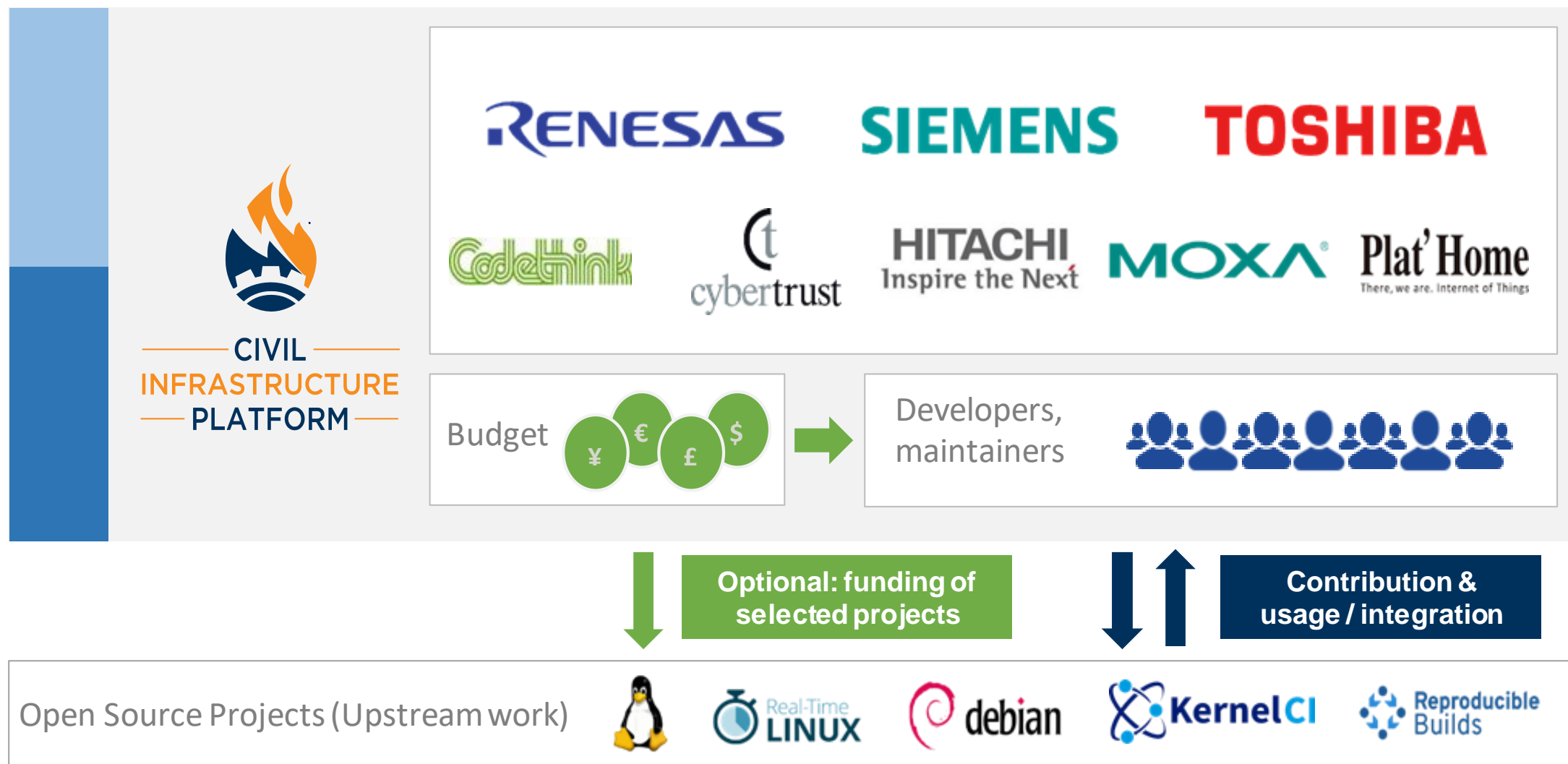
SLTS Super Long Term Support

CIP governance structure and projects

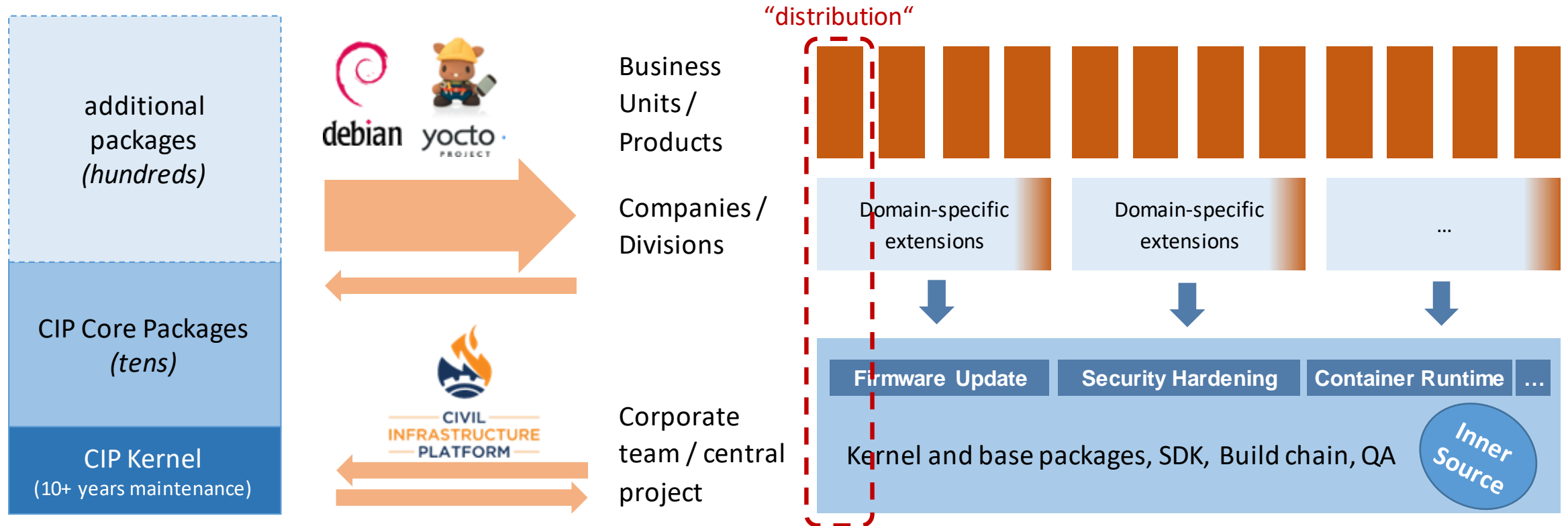


CIP Projects and its scopes

The backbone of CIP are the member companies



Mapping CIP into the company



Up to 70% effort reduction achievable for OSS license clearing and vulnerability monitoring, kernel and package maintenance, application adaptation and testing for an individual product.

Upstream First

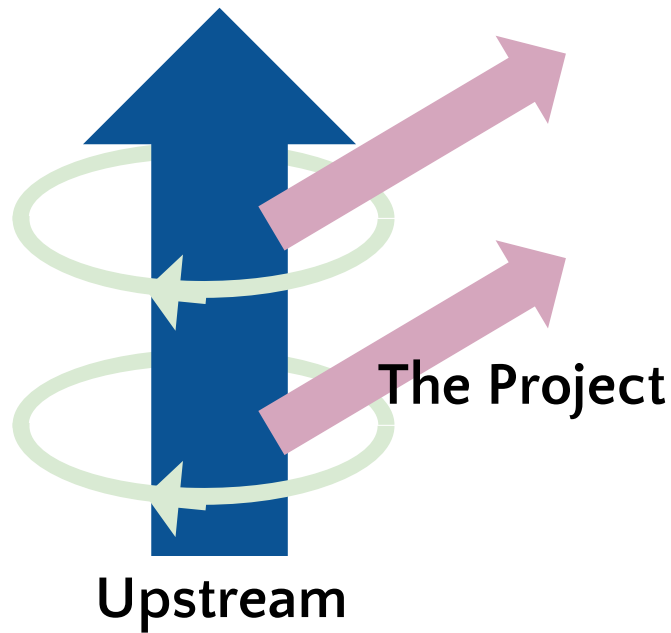


Development Models



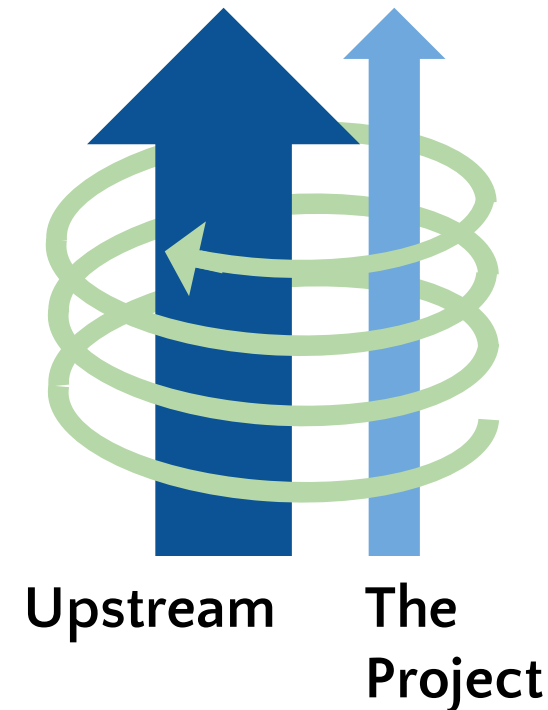
“Own Community” Model

Branches its base from upstream and evolves by its own.



“Upstream First” Model

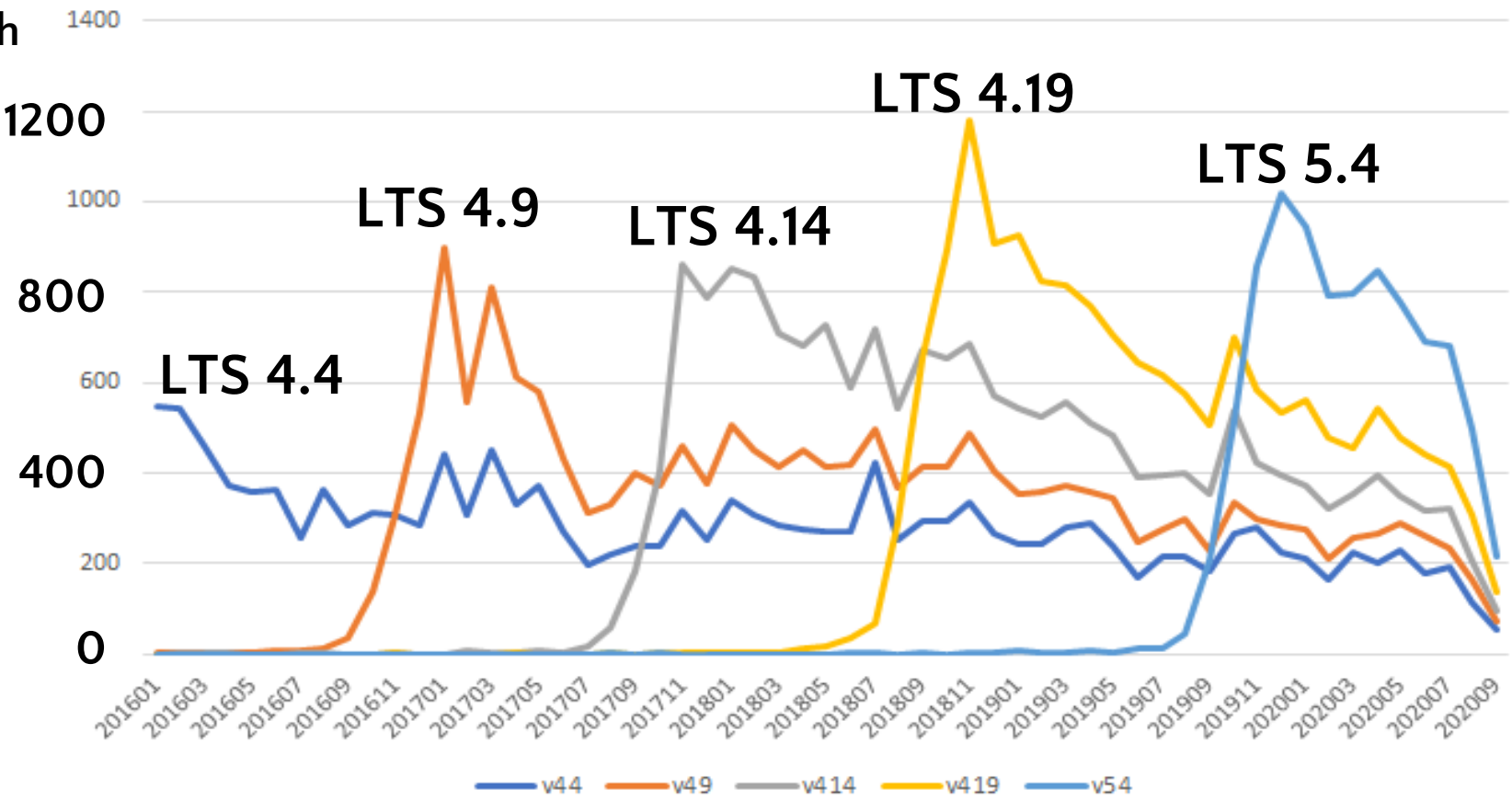
Only allows patch commits if those patches are already in the upstream.



Commit Counts per LTS



Commit Counts
per Month

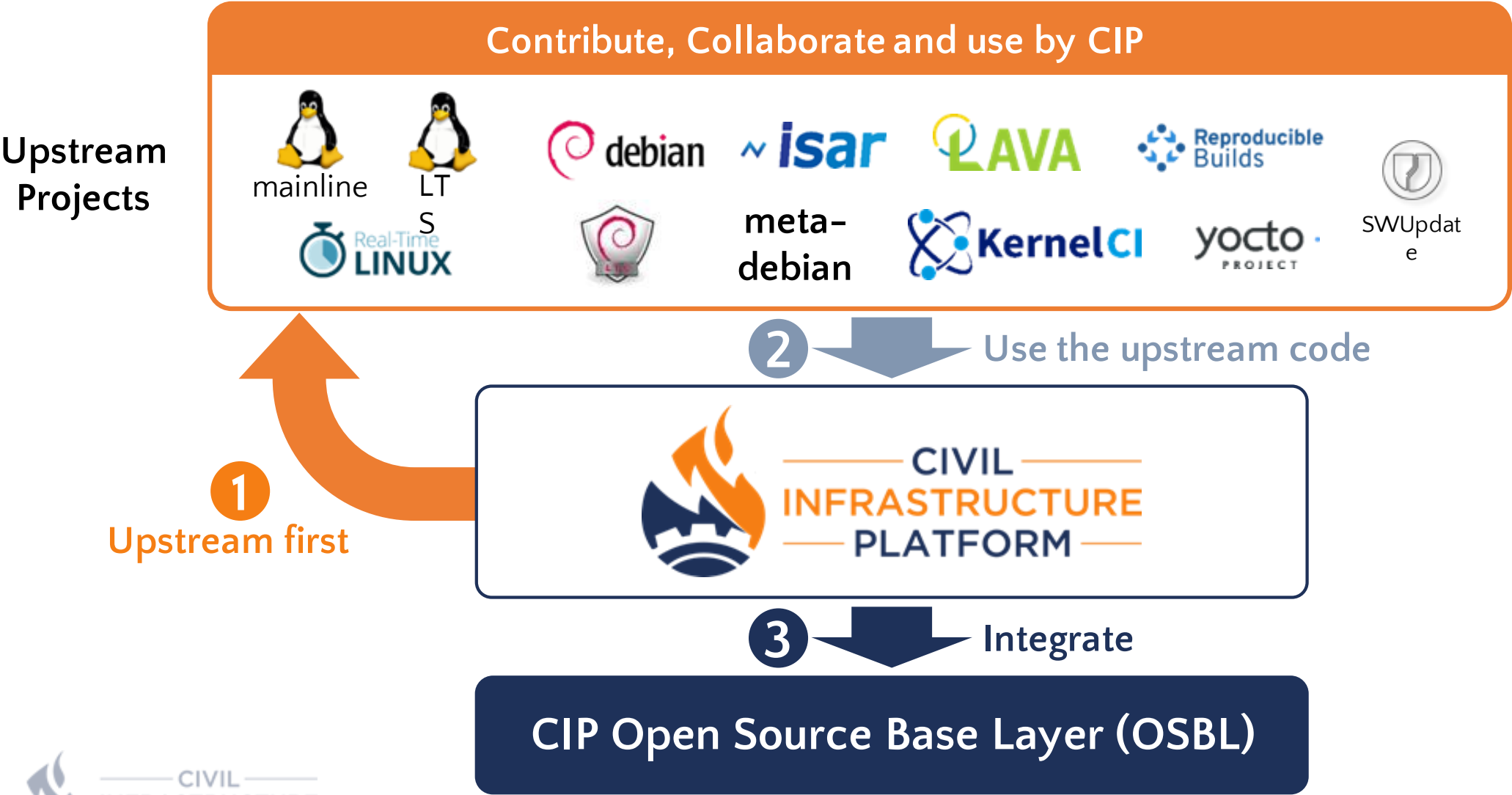


Year/Month



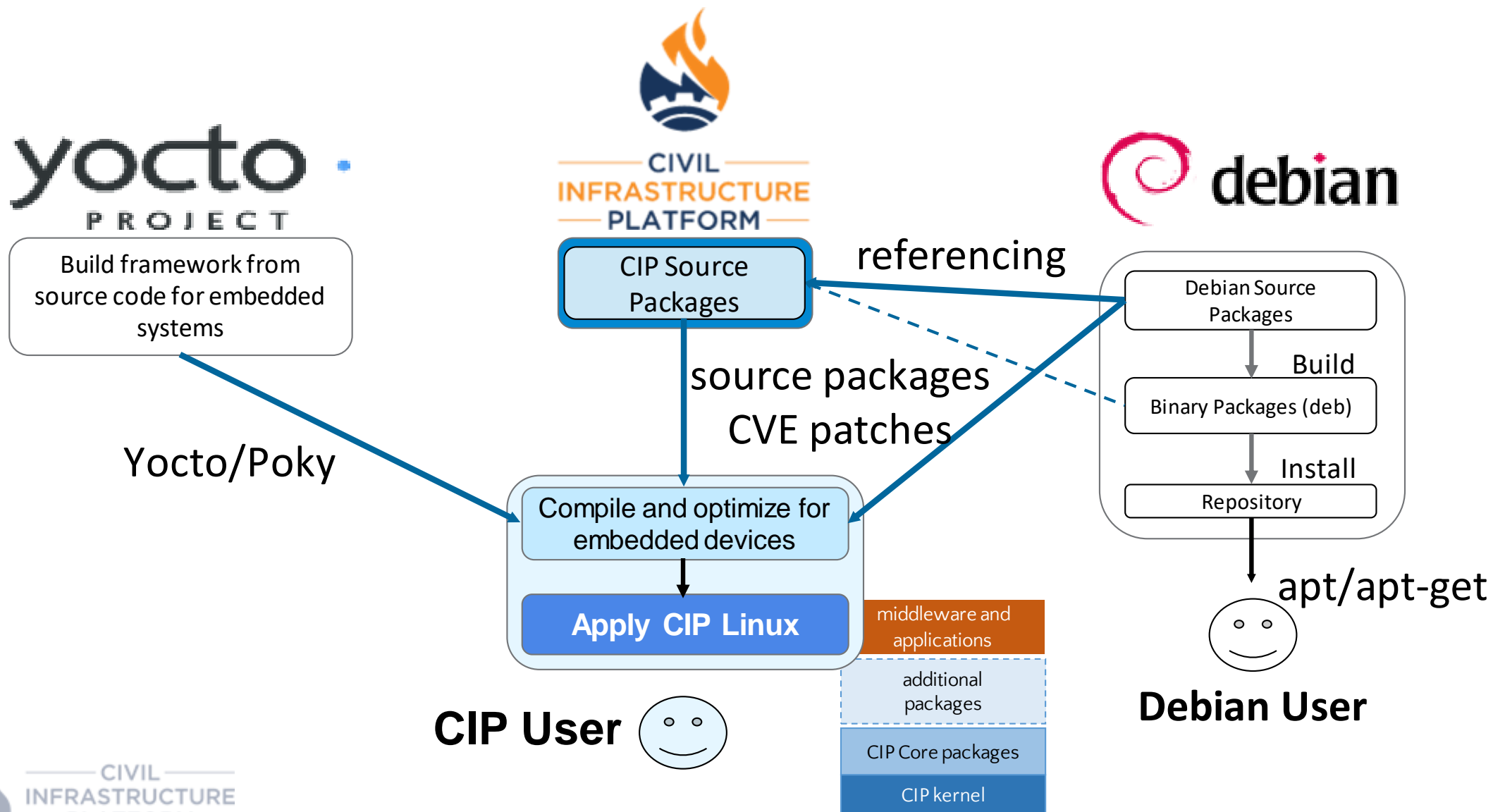
Note: If a patch has an original patch, the date of the patch is that of the original one.

Collaborative development with other OSS projects





How CIP Artifacts can be used



CIP Kernel Team Activities





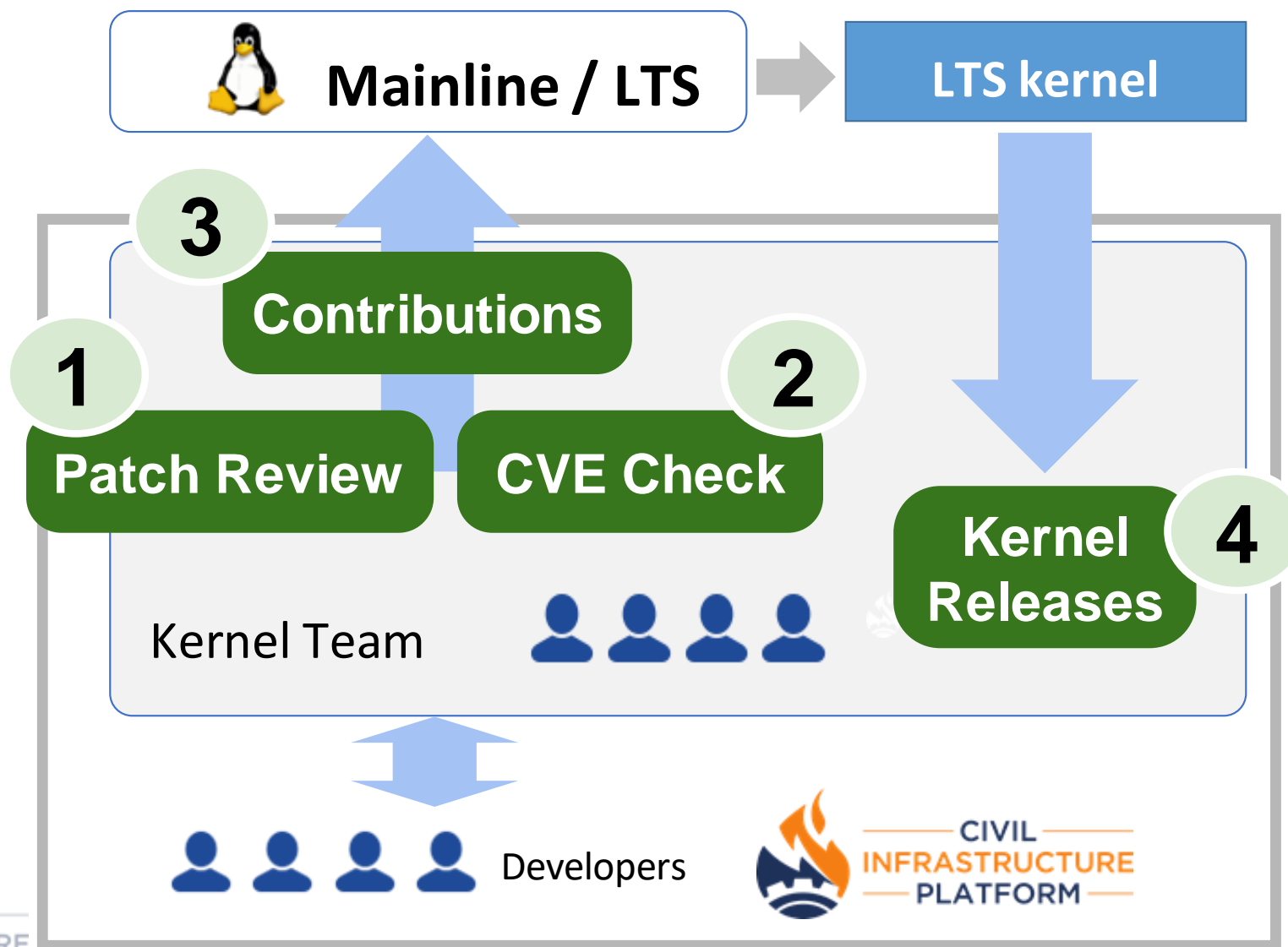
Primary Goal

- Provide CIP SLTS kernels with ten+ years maintenance period **by fixing versions** to fulfill the required level of reliability, sustainability, and security

Team Members

- Masashi Kudo – **Chairperson**
- Nobuhiro Iwamatsu – **Kernel Maintainer**
- Pavel Machek – **Kernel Maintainer**
- Ben Hutchings – **Kernel Mentor**
- Chen-Yu Tsai – **Kernel Developer**

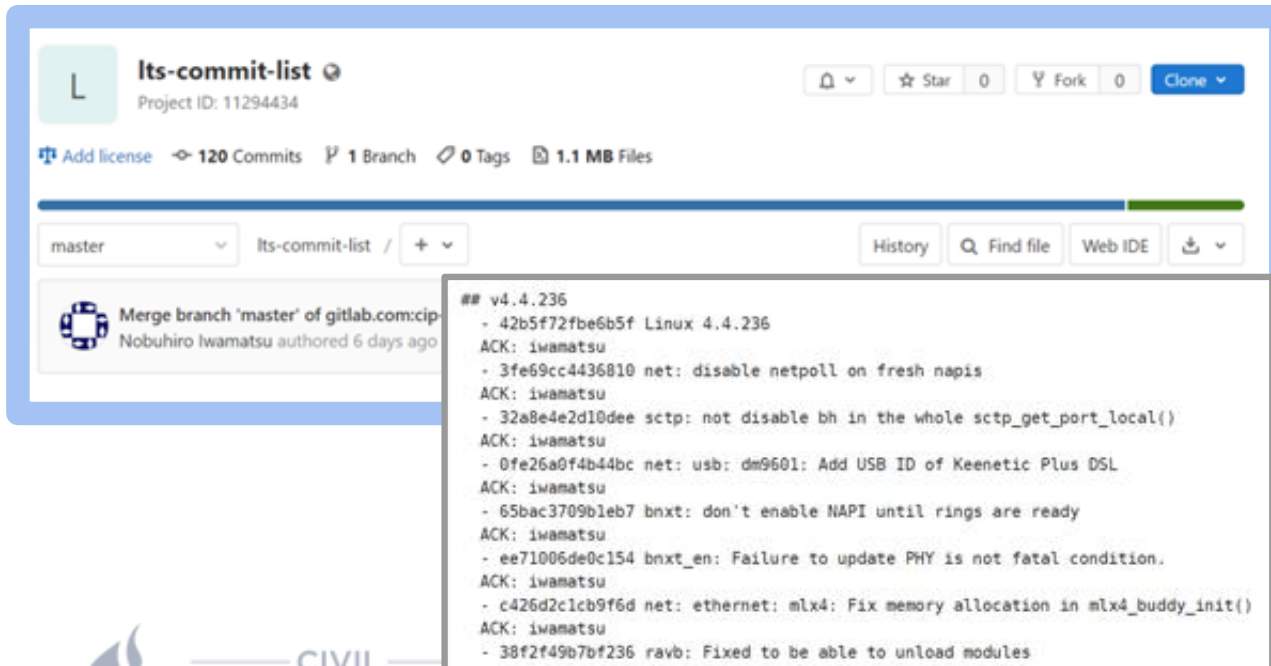
CIP SLTS kernel development



1 Stable Patches Review

Stable Patch Review

- Reviews for -rc
 - Review results are posted to ML
- Reviews for stable releases



```
> --- a/drivers/infiniband/core/restrack.c
> +++ b/drivers/infiniband/core/restrack.c
> @@ -209,7 +209,7 @@ void rdma_restrack_del(struct rdma_restr
>     struct ib_device *dev;
>
>     if (!res->valid)
> -         return;
> +         goto out;
>
>     dev = res_to_dev(res);
>     if (!dev)
> #         return;
```

This test does return, does it need to go through 'goto out', too? (I see it should not happen, but...)

```
> @@ -222,8 +222,10 @@ void rdma_restrack_del(struct rdma_restr
>     down_write(&dev->res.rwsem);
>     hash_del(&res->node);
>     res->valid = false;
> +     up_write(&dev->res.rwsem);
> +
> +out:
>     if (res->task)
>         put_task_struct(res->task);
> -     up_write(&dev->res.rwsem);
> }
```

Mainline says res->task = NULL is needed there, see fe9bc1644918aa1d.

Best regards,

Pavel

2 CVE Check



Mainline/
LTS



debian



ubuntu

Gather kernel CVE Information



cip-kernel-sec

Project ID: 3189019 | Request Access

Linux Kernel Cve + 1 more



Star

3



Fork

4



420 Commits



5 Branches



0 Tags



2.7 MB Files



2.7 MB Storage

cip-kernel-sec



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Open Issues

CVE ID	Severity	Status	Assignee	Created	Updated
CVE-2016-0850	High	Open		2016-08-05	2016-08-05
CVE-2016-0851	High	Open		2016-08-05	2016-08-05
CVE-2016-0852	High	Open		2016-08-05	2016-08-05
CVE-2016-0853	High	Open		2016-08-05	2016-08-05
CVE-2016-0854	High	Open		2016-08-05	2016-08-05
CVE-2016-0855	High	Open		2016-08-05	2016-08-05
CVE-2016-0856	High	Open		2016-08-05	2016-08-05
CVE-2016-0857	High	Open		2016-08-05	2016-08-05
CVE-2016-0858	High	Open		2016-08-05	2016-08-05
CVE-2016-0859	High	Open		2016-08-05	2016-08-05

cip-kernel-sec I/F

privileges by mounting an overlayfs filesystem on top of a FUSE filesystem, and then executing a crafted setuid program.
CVE-2016-0860 => local DoS due to a page lock order bug in the XFS seek hole/data implementation
CVE-2016-10723 => Don't call schedule_timeout_killable() with oom_lock held
CVE-2016-10741 => xfs: BUG_ON in __xfs_get_blocks() with xfstests generic/446

Analyse CVEs to determine necessities for contributions

Refer

cip-kernel-config



cip-kernel-config

Project ID: 6052798

Linux Kernel Cip



Star

2



Fork

9



126 Commits



4 Branches










0 Tags

2 cip-kernel-sec


- Tracks the status of security issues, identified by CVE ID, in mainline, stable, and other configured branches.

cip-project > cip-kernel > cip-kernel-sec > Details

**cip-kernel-sec** 
Project ID: 3189019
[Linux](#) [Kernel](#) [Cve](#) + 1 more

 **LICENSE**  **284 Commits**  **1 Branch**  **0 Tags**  **1.5 MB Files**

Linux kernel CVE tracker

**Auto DevOps**
It will automatically build, test, and deploy your application based on a predefined CI/CD configuration.
[Learn more in the Auto DevOps documentation](#)
[Enable in settings](#)


master

cip-kernel-sec / +

History

Find file

Web IDE

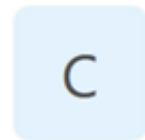
 Merge branch 'bwh/update-issues' into 'master' ...
SZ Lin (林上智) authored 1 week ago
f2989df1







2 cip-kernel-config






- Necessity of contributions (backporting) is determined to be fixed base on kernel configurations provided by CIP members

 cip-project >  cip-kernel > cip-kernel-config > Details



cip-kernel-config 
Project ID: 6052798
 Linux  Kernel  Cip

☆ Star 1 Clone ▾


 GNU GPLv2  89 Commits  4 Branches  0 Tags  502 KB Files


Kernel configurations provided by CIP Members



master ▾


cip-kernel-config

History

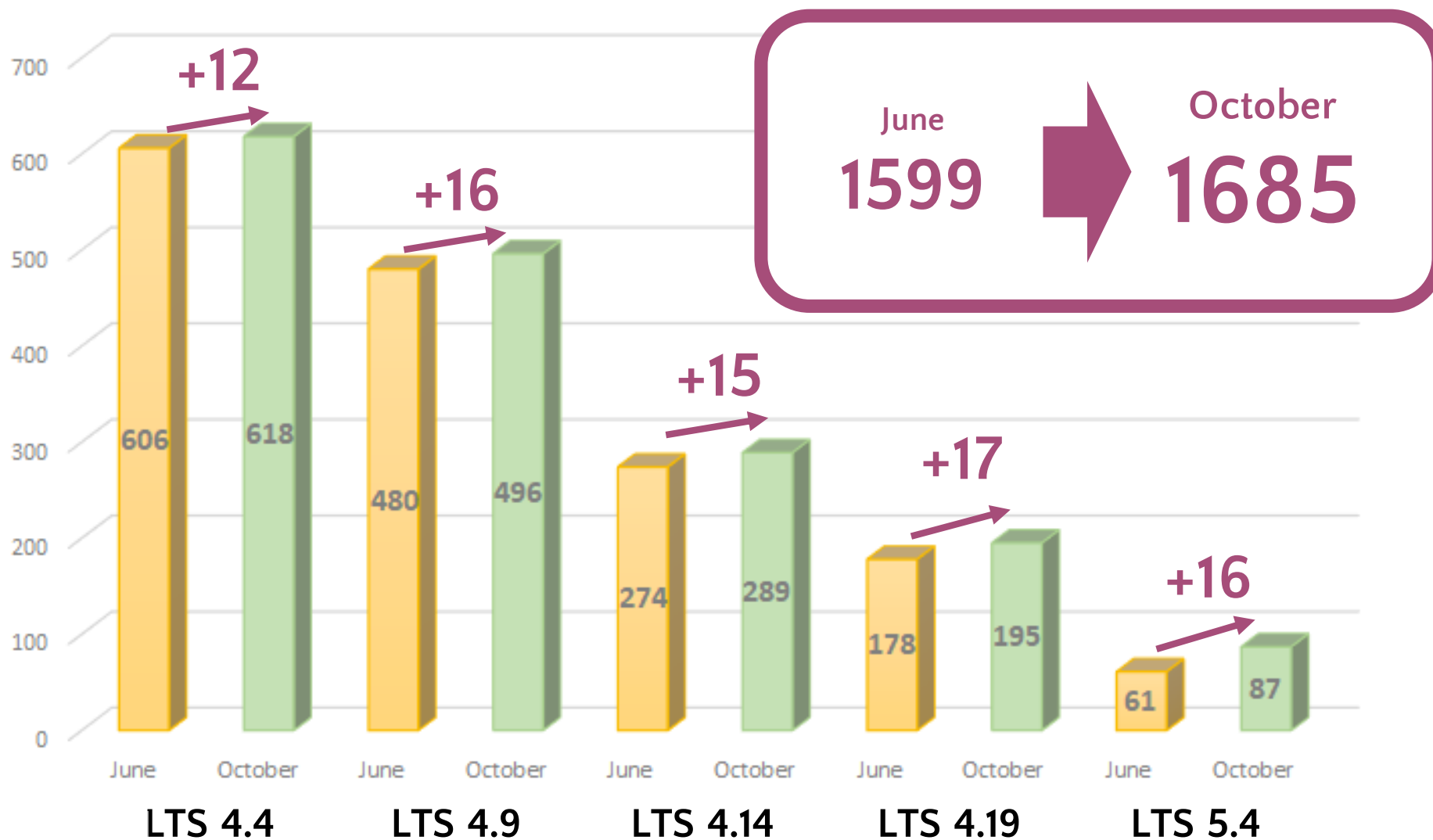
 Find file

 ▾

 Merge branch 'iwamatsu/update-configs-renesas' into 'master' 

c4f7a24b 

Nobuhiro Iwamatsu authored 1 week ago



3 Contributions to LTS – Details

as of October 6, 2020



	v4.4.238	v4.9.238	v4.14.200	v4.19.149	v5.4.69	TOTAL
Suggested-by:	1	1	1	2	1	6
Reported-by:	44	35	29	16	6	130
Signed-off-by:	440	334	149	88	41	1052
Debugged-by:	1	1				2
Author:	80	83	55	39	23	280
Acked-by:	26	29	33	44	13	145
Reviewed-by:	2	4	10	7	6	29
Tested-by:	4	4	6	3		17
Cc:	104	97	72	51	28	352
TOTAL	618	496	289	195	87	1685

Note: There could be multiple contributions by a same personnel in one commit. such duplicates are eliminated in total numbers. Therefore, the summation of each item may not equal to “Total”.

4 CIP Kernel Release Process



1. Review stable patches – status tracked in Gitlab [1]
 - Mark the review and the name of the worker under the commit.
 - **Start to review stable kernel patches in rc stage**
2. Review patch from CIP members via cip-dev [2]
 - Update the status of the commit in patchwork
3. Start testing
4. Tag release candidate
5. Ack by other maintainers
6. Release and send the news to cip-dev

[1] <https://gitlab.com/cip-project/cip-kernel/lts-commit-list>

[2] <https://patchwork.kernel.org/project/cip-dev/list/>

```
# Stable Kernel Patches Review Status
Please list your name and review result below the patch item

* UR: Under Review
* ACK: Acknowledge (if the patch is accepted)
* TBB: To be backported (if other patches should be also backported)
* NAK: Negative acknowledge (if the patch is rejected, please list the reason)
* IGN: Patch was not reviewed as it is out of scope for CIP project

## v4.4.184
- 72d1ee93e931 Linux 4.4.184
- 46c7b5d6f2a5 tcp: refine memory limit test in tcp_fragment()
```

4

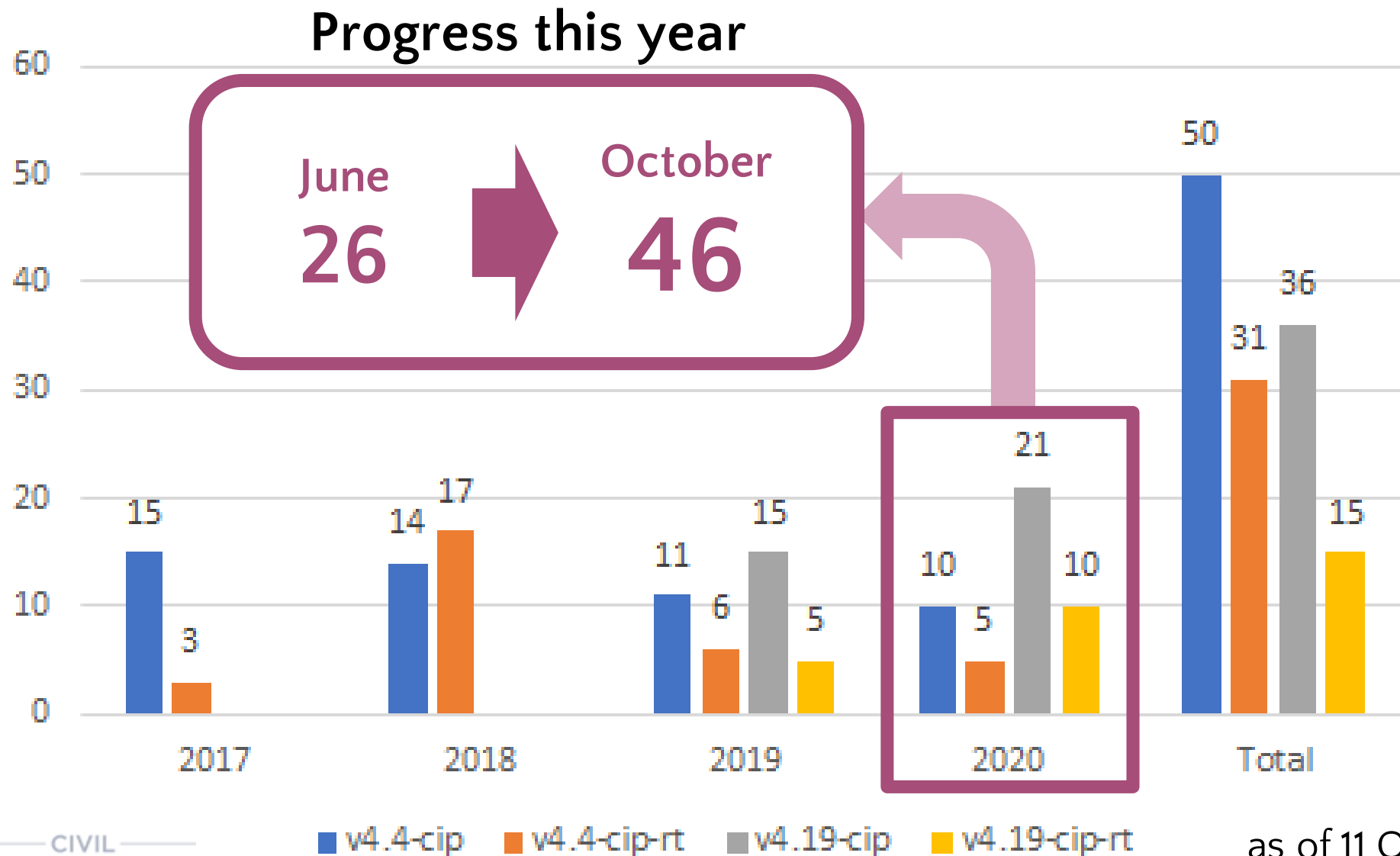
CIP SLTS Kernel Release Policy



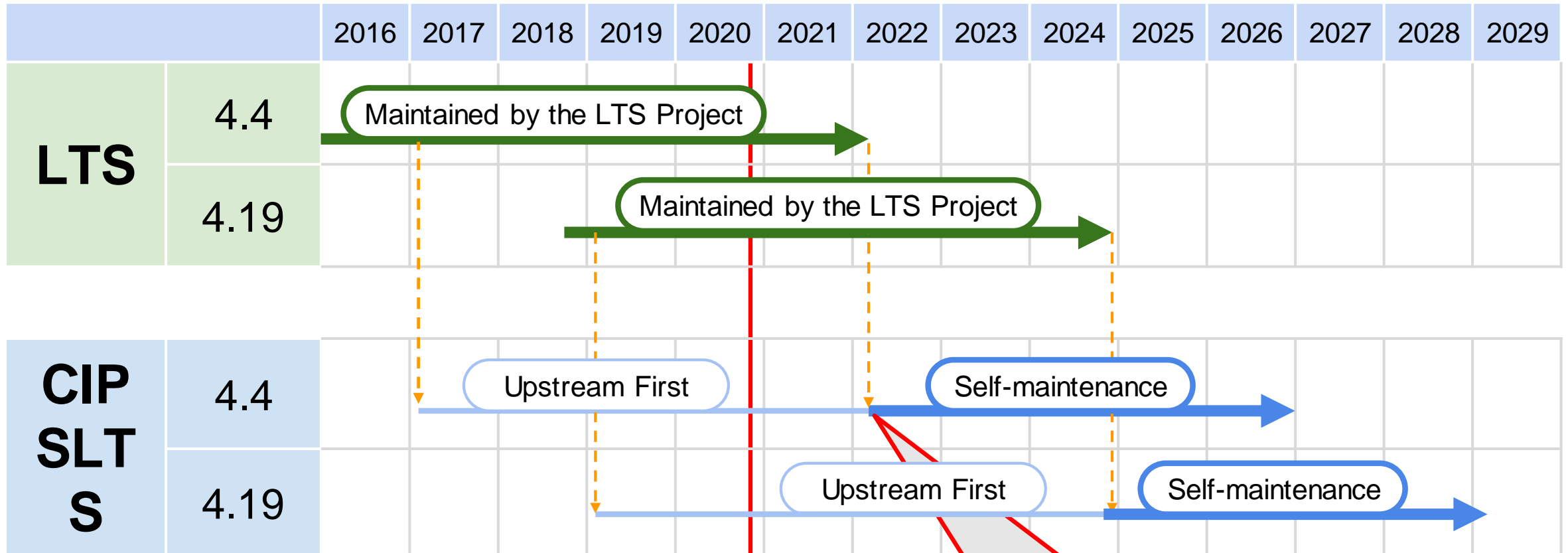
Current Releases		Life-Cycle		Release Frequency	
		First Release	Projected EOL	Regular Release	Release on Demand
SLTS 4.4	SLTS 4.4	2017-01-17	2027-01	once a month	Depends on criticality of bug / security fixes
	SLTS 4.4-rt	2017-11-16	2027-01	once every two months	
SLTS 4.19	SLTS 4.19	2019-01-11	2029-01	twice a month	
	SLTS 4.19-rt	2019-01-11	2029-01	once every two months	

Note: Difficult to estimate actual release date because of number of patches depends on each stable release

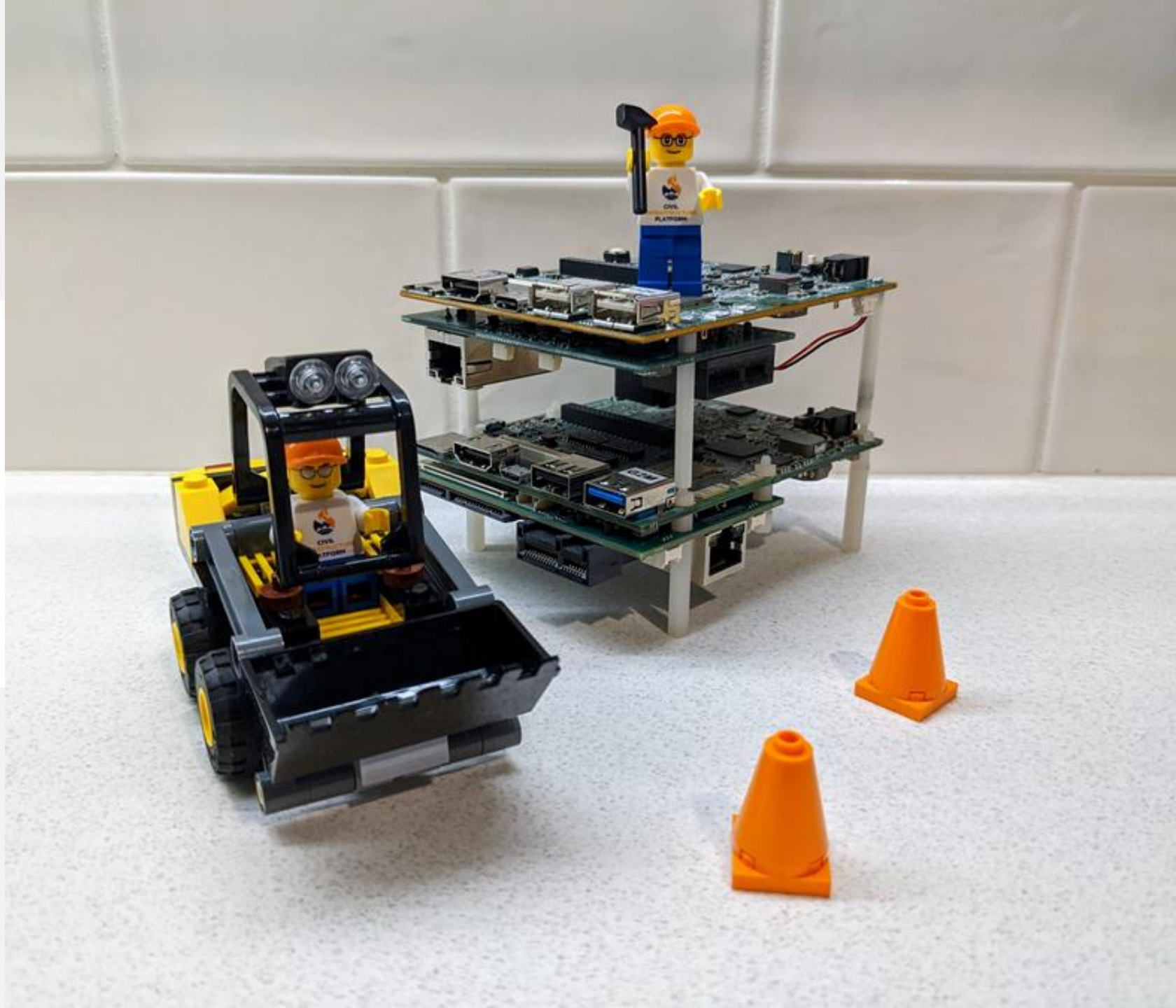
4 CIP SLTS Kernel Release Statistics



CIP SLTS Kernel Maintenance



CIP Automated Testing

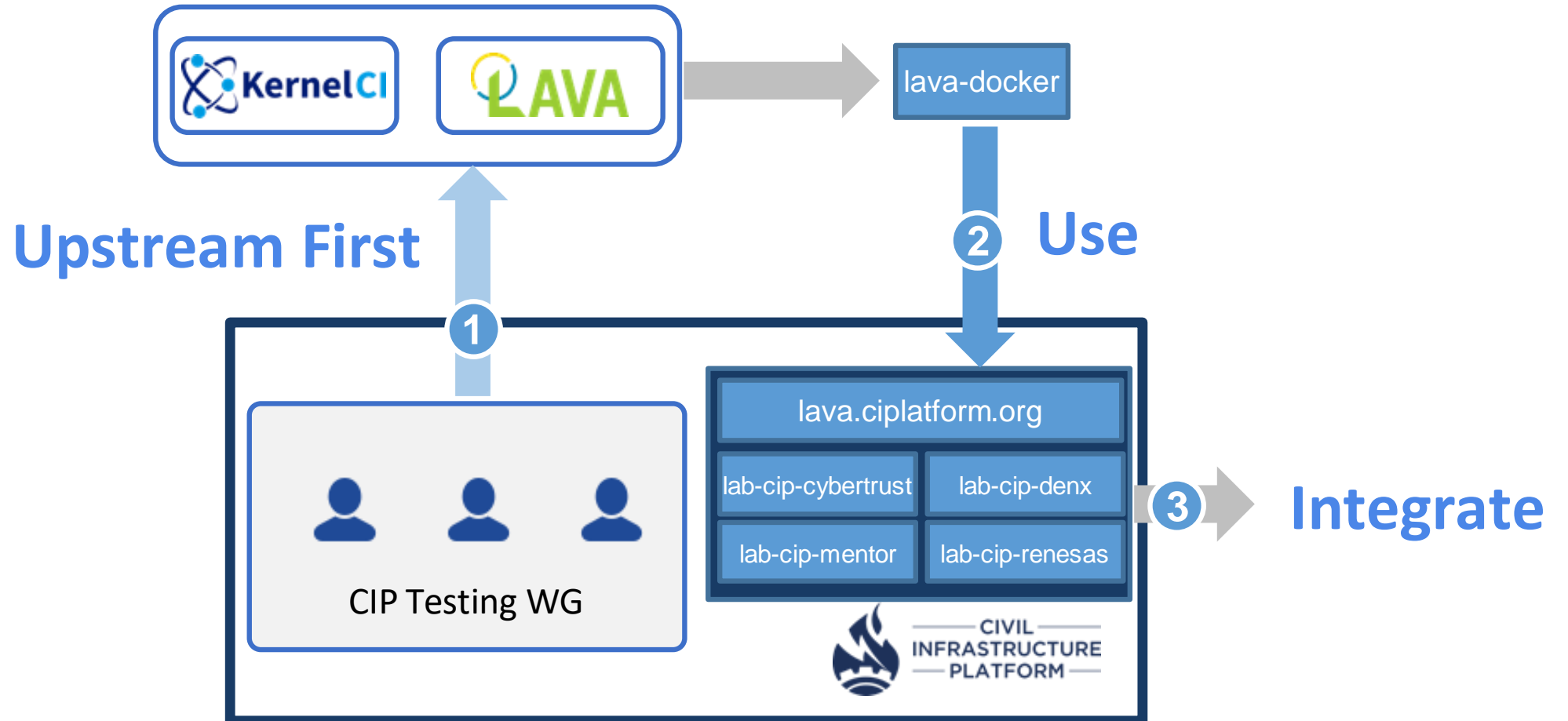


Testing Goals

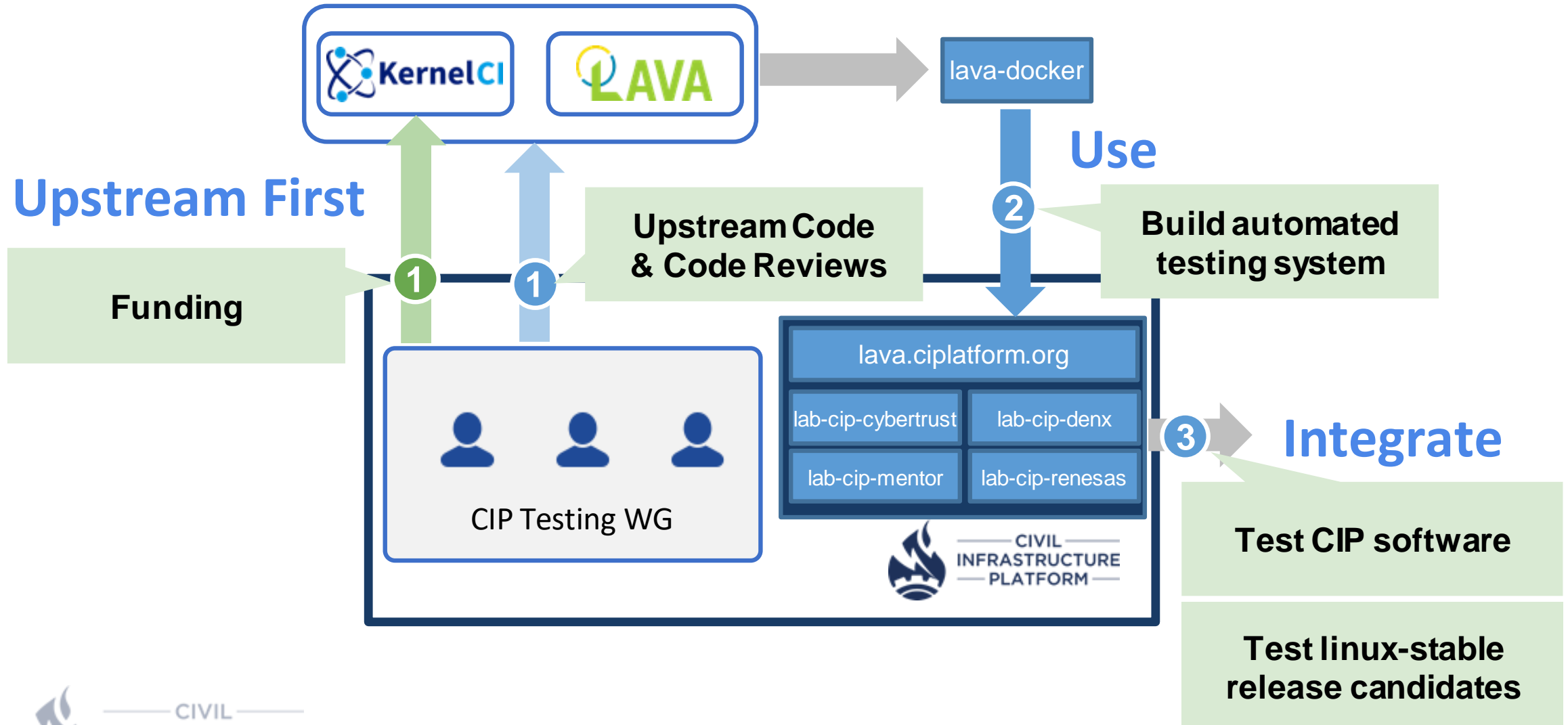


- **Centralised control / distributed testing**
 - CIP developers who are distributed over the world should be able to test CIP software on the CIP reference platforms, even if they don't have a platform locally
- **Automated testing with Continuous Integration (CI)**
 - Sustain periodical and long-term kernel releases cost-effectively
- **Open Source collaboration**
 - Improve the whole ecosystem and avoid reinventing the wheel

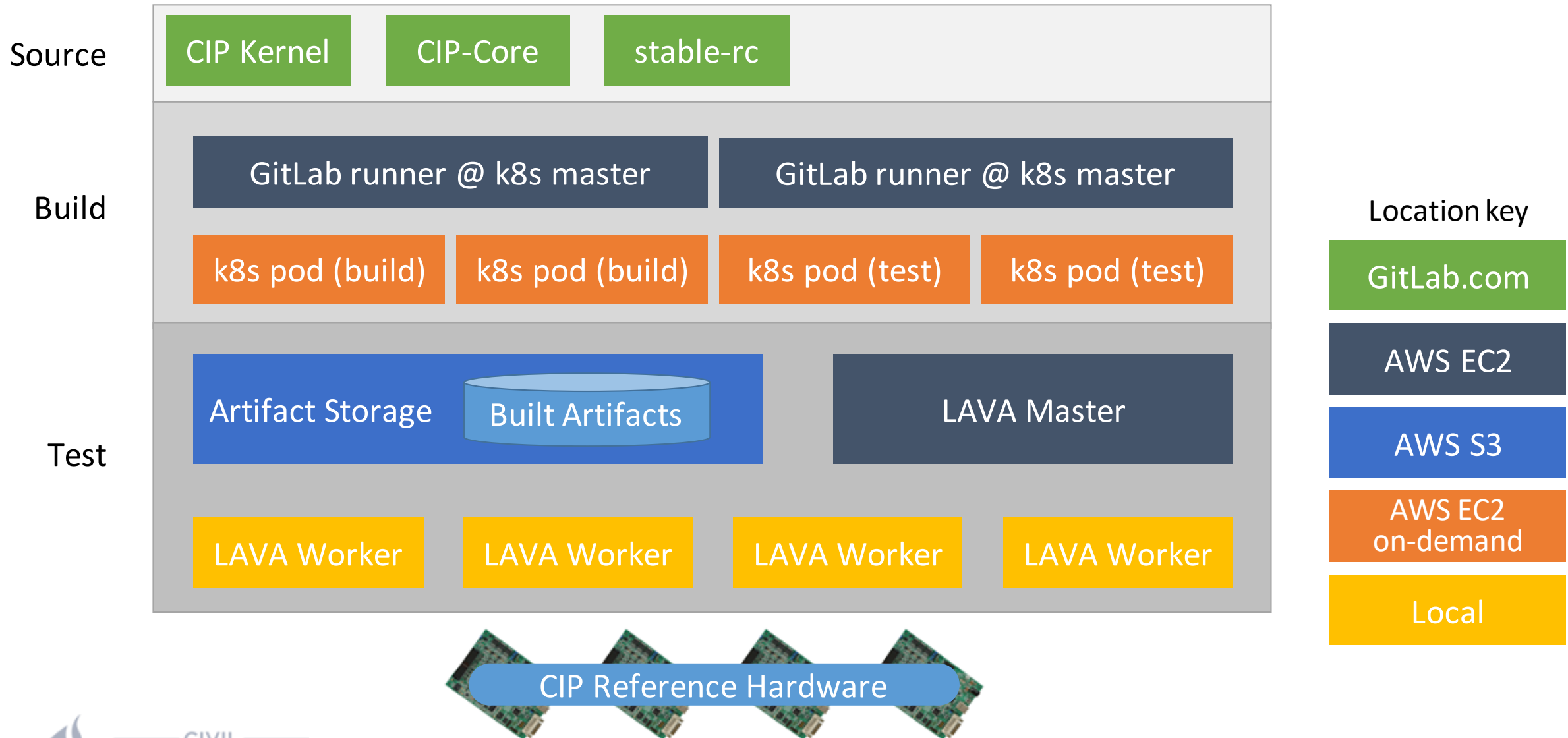
Open Source Approach



Open Source Approach - In Practice



Testing Architecture Overview



CIP Reference Boards



CIP Reference Boards		Supported Kernels			
Platform	Architecture	SLTS v4.4	SLTS v4.4-rt	SLTS v4.19	SLTS v4.19-rt
AM335x Beaglebone Black	Armv7	Y	Y ¹	Y	Y ¹
Cyclone V DE0-Nano-SoC Development Kit	Armv7	N	N	Y	Y ¹
QEMU	x86_64	Y	Y ¹	Y	Y ¹
RZ/G1M iWave Qseven Development Kit	Armv7	Y	Y ^{1,2}	Y	Y ^{1,2}
RZ/G2M HopeRun HiHope	Armv8	N	N	Y	Y ^{1,2}
SIMATIC IPC227E	x86_64	N	N	Y	Y ¹
OpenBlocks IoT VX2	x86_64	N	N	Y	Y ¹

CIP Reference Board Candidates		Supported Kernels			
Platform	Architecture	SLTS v4.4	SLTS v4.4-rt	SLTS v4.19	SLTS v4.19-rt
Zynq UltraScale+ MPSoC ZCU102 Evaluation Kit	Armv8	N	N	Y	Y ¹

¹ Tested with standard Kernel configuration (non-RT)

² Tested with Real-Time enabled Kernel configuration



- Currently CIP is running the following tests:
 - Boot test
 - `uname -a`
 - Spectre/Meltdown checker
 - A shell script to tell if your system is vulnerable against the several "speculative execution" CVEs that were made public in 2018.
 - <https://github.com/Linaro/test-definitions/tree/master/automated/linux/spectre-meltdown-checker-test>
 - LTP
 - ltp-cve-tests, ltp-dio-tests, ltp-fs-tests, ltp-ipc-tests, ltp-math-tests, ltp-open-posix-tests, ltp-sched-tests, ltp-syscalls-tests and ltp-timers-tests
 - <https://github.com/Linaro/test-definitions/tree/master/automated/linux/ltp>
 - <https://github.com/Linaro/test-definitions/tree/master/automated/linux/ltp-open-posix>
 - Cyclictest+Hackbench
 - This test measures event latency in the Linux Kernel, with hackbench running in the background to stress the system.
 - https://gitlab.com/cip-project/cip-testing/linux-cip-ci/-/blob/master/lava_templates/test_cyclictest+hackbench.yaml



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Test Results in LAVA



LAVA / Scheduler / Jobs

All Jobs

● All Jobs ▶ Active Jobs || Queued Jobs ♥ Health

Show 25 entries

ID	Actions	State	Device	Device type	Description
17602		Complete	zynqmp-zcu102-01	zynqmp-zcu102	zynqmp-zcu102 healthcheck
17601		Complete	qemu-03	qemu	qemu x86_64 healthcheck
17583		Complete	r8a7743-lwg20d-q7-01	r8a7743-lwg20d-q7	r8a7743-lwg20d-q7 healthcheck
17582		Complete	zynqmp-zcu102-01	zynqmp-zcu102	zynqmp-zcu102 healthcheck
17581		Complete	qemu-03	qemu	qemu x86_64 healthcheck
17580		Complete	qemu-01	qemu	ci-iwamatsu-linux-4.4.y-cip-rc_bzimage_cip_qemu_defconfig_4.4.222-cip45_a5f3949c_x86_cip_qemu_defconfig_ltp
17579		Complete	qemu-04	qemu	ci-iwamatsu-linux-4.4.y-cip-rc_bzimage_cip_qemu_defconfig_4.4.222-cip45_a5f3949c_x86_cip_qemu_defconfig_ltp

LAVA / Results / Test job 17580 / Suite 2_ltp-timers-tests

Results for test suite 2_ltp-timers-tests - Test Job 17580

Exports ?

Test suite export : CSV or YAML

Show 25 entries

Search

Name	Test Set	Result	Measurement	Units	Logged	Bug Links
timer_create02	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_create03	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_create04	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_delete02	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_delete03	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_settime02	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
timer_settime03	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]
leapsec_timer	—	✓ pass	—	—	06/08/2020 12:27 a.m.	[0]

Collaboration with KernelCI



- CIP joined the [KernelCI project](#) at its inception in 2019.
- As premier members we help to manage and steer the project.
- We also contribute via code and code reviews, and plan to keep improving the project in this way.
- In next few months we plan to start using the KernelCI front end to help us visualise our build and test results.
- If you want to learn more about KernelCI please attend [Guillaume's BoF](#) later today or [Khouloud's talk](#) on Wednesday evening.

Summary



Summary



- CIP Kernel and Test Teams follows “Upstream First” principle, and contributes to upstream.
- By taking advantage of kernel LTS, the team steadily releases CIP SLTS kernels, and aims to maintain them for 10 years or more.
- To reduce CIP SLTS kernel release cost, the team is closely working with CIP testing team to build automated testing systems.



Please join us
to sustain
Civil Infrastructure
together !

Weekly Regular Online Meeting



- CIP IRC weekly meeting – Every Thursday UTC (GMT) 09:00

US-West	US-East	UK	DE	TW	JP
02:00	05:00	10:00	11:00	17:00	18:00

- Channel:

* `irc:chat.freenode.net:6667/cip`

- The meeting is used to share status among CIP developers (Kernel Team, Test Team, SW Update WG, Security WG)

CIP Kernel Workgroup Repositories



- CIP Linux kernel & real-time kernel
 - <https://git.kernel.org/pub/scm/linux/kernel/git/cip/linux-cip.git>
- CIP Linux kernel CVE tracker
 - <https://gitlab.com/cip-project/cip-kernel/cip-kernel-sec>
- CIP Linux kernel failed patches tracker
 - <https://gitlab.com/cip-project/cip-kernel/classify-failed-patches>

CIP Testing Workgroup Links



- CIP Testing WG **wiki page**
 - <https://wiki.linuxfoundation.org/civilinfrastructureplatform/ciptesting/ciptestingwg>
- **CIP LAVA master**
 - <https://lava.ciplatform.org/>
- CIP's fork of **lava-docker**
 - <https://gitlab.com/cip-project/cip-testing/lava-docker>
- **GitLab Cloud CI** – manages our k8s build pods
 - <https://gitlab.com/cip-project/cip-testing/gitlab-cloud-ci>
- **CIP CI** – scripts used to build and test the Kernel
 - <https://gitlab.com/cip-project/cip-testing/linux-cip-ci>

Contact Information and Resources



To get the latest information, please contact:

- CIP Mailing List: cip-dev@lists.cip-project.org

Other resources

- Twitter: [@cip_project](https://twitter.com/cip_project)
- CIP Web Site: <https://www.cip-project.org>
- CIP News: <https://www.cip-project.org/news/in-the-news>
- CIP Wiki: <https://wiki.linuxfoundation.org/civilinfrastructureplatform/>
- CIP Source Code
 - CIP repositories hosted at kernel.org: <https://git.kernel.org/pub/scm/linux/kernel/git/cip/>
 - CIP GitLab: <https://gitlab.com/cip-project>



Thank You

Upcoming CIP Sessions

- **CIP Mini summit**

- **Friday, October 30 • 11:00 - 12:30**

- **Other CIP members talks**

- **The International Effort to Establish Open Source Base Layer of Cyber Security for IACS**
 - Kento Yoshida, Renesas Electronics Corporation
 - **Wednesday, October 28 • 16:15 - 17:05**
- **Threat Modelling - Key Methodologies and Applications from OSS CIP Perspective**
 - Dinesh Kumar, Toshiba Software India & SZ Lin, Moxa Inc
 - **Tuesday, October 27 • 14:15 - 15:05**