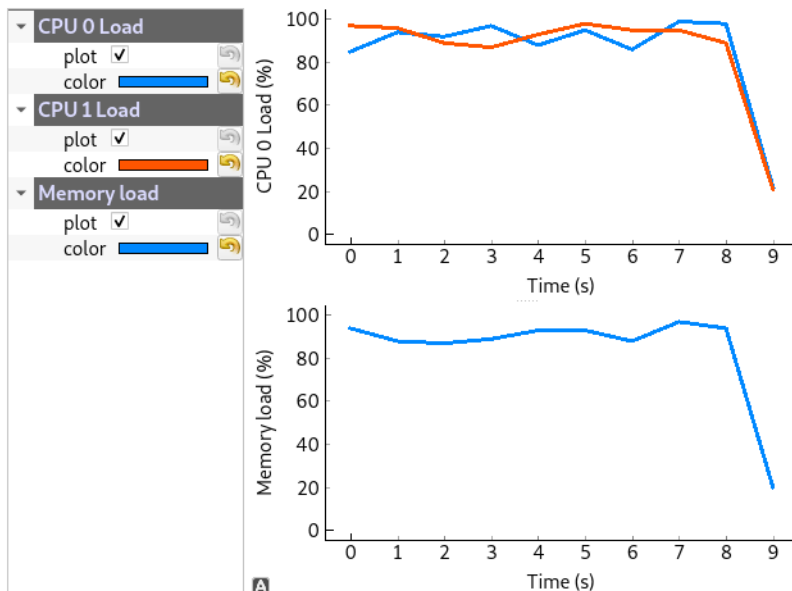# Non-Intrusive Power/Perf. Debugging via JTAG

Alexandre Bailon / Patrick Titiano

What is demonstrated

Non-intrusive OS-agnostic Real-time Monitoring of SoC PMUs (CPU, DDR)

Hardware Information

JTAG, J-Link, ARM® CoreSight™

What was improved

- Smart Debugging/Monitoring Framework leveraging
  - JTAG interface
  - SVD register definition files
  - OpenOCD libs

- Real-Time
- Non-intrusive
- OS-agnostic
- Multi_platform
- Generic Core / Versatile apps.

  Want to know more? Just ask!

Source code or detail technical information availability

https://github.com/BayLibre/pmugraph
https://github.com/BayLibre/libregice