

MINNOWMAX LURE - SEACAT REV_A

POWER SUPPLY ESTIMATES (2 CARDS RUNNING):
SOURCE
 VDD5V - 5.0V / 1838mA (80% efficiency)
 GPIO_PROG_VOLTAGE - 3.3V/52mA
LOADS
 3V3_PCIE - 3.3V / 1000mA
 1V5_PCIE - 1.5V / 250mA
 3V3_mSATA - 3.3V / 1000mA
 1V5_mSATA - 1.5V / 250mA

BOARD HISTORY

DATE 04/18/2014 INITIAL VERSION - REV_A

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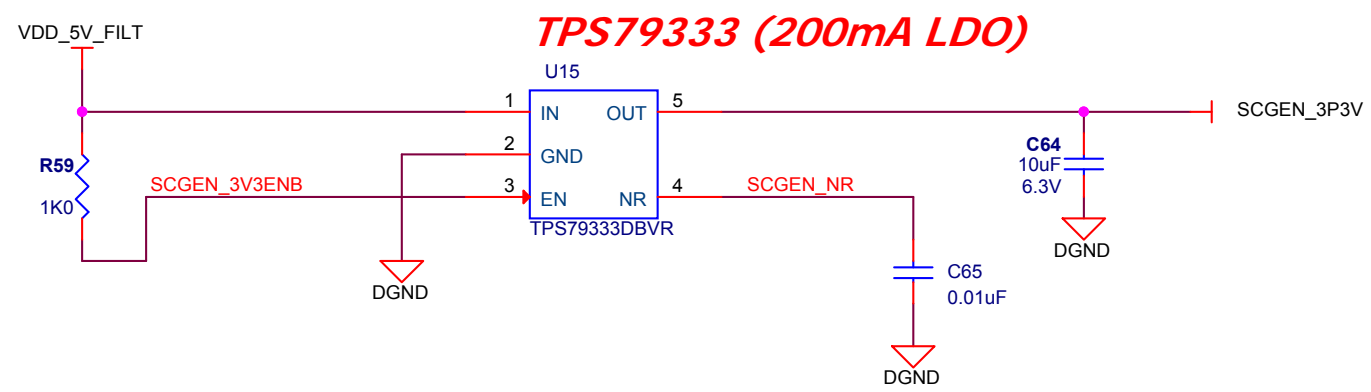
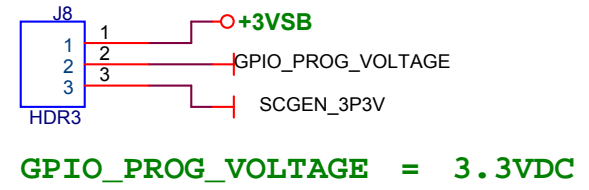
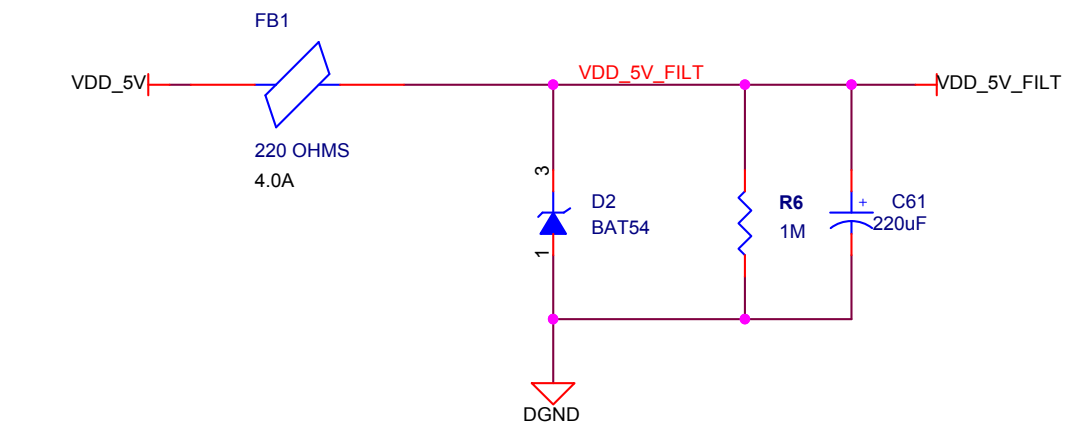
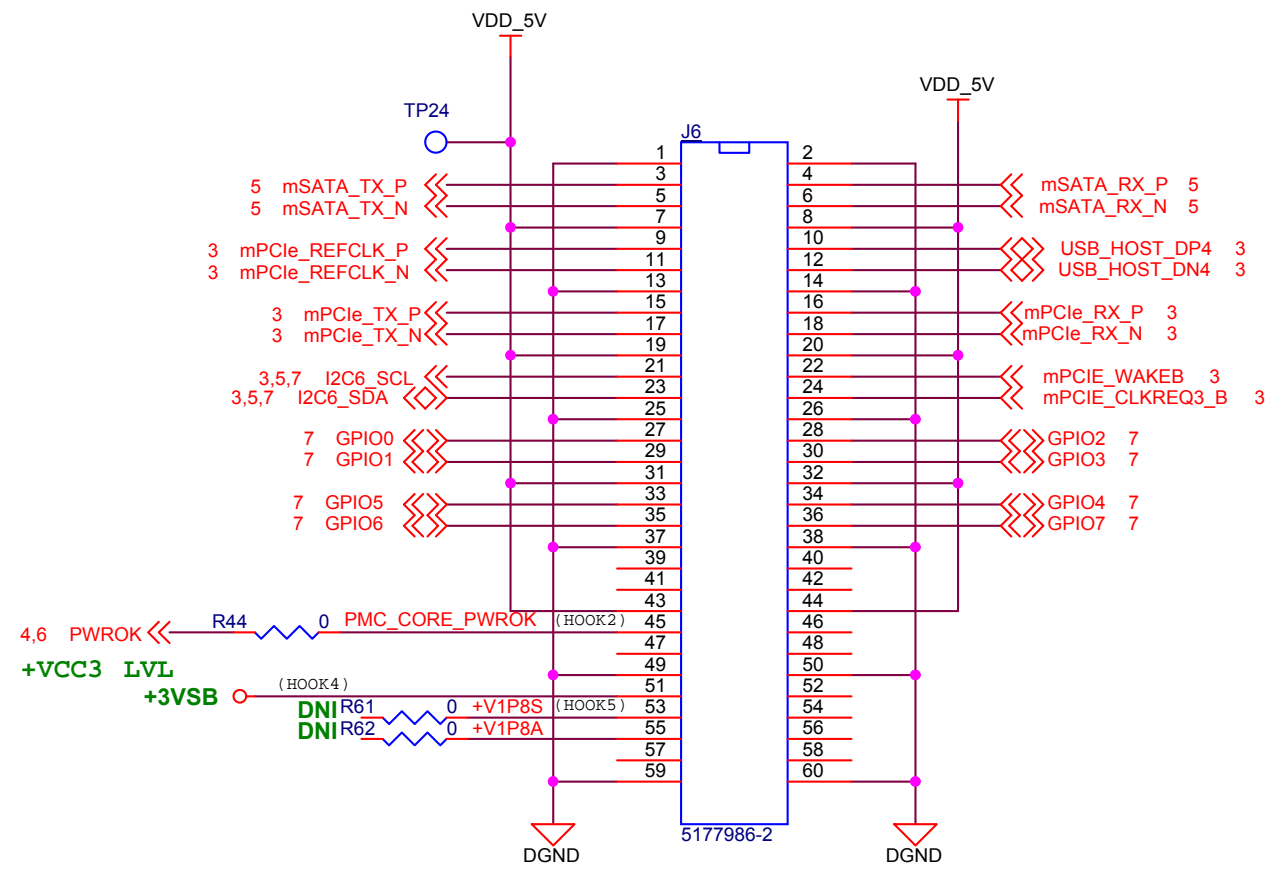
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BLOCK DIAGRAM

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Size	Date Modified	DWG NO	Rev	
B	Friday, April 18, 2014	002-0004993	A	
Engineer(s)			Sheet	
MINNOW_TEAM			1	of 7

1380 Presidential Dr Ste#100
 Richardson, TX 75081

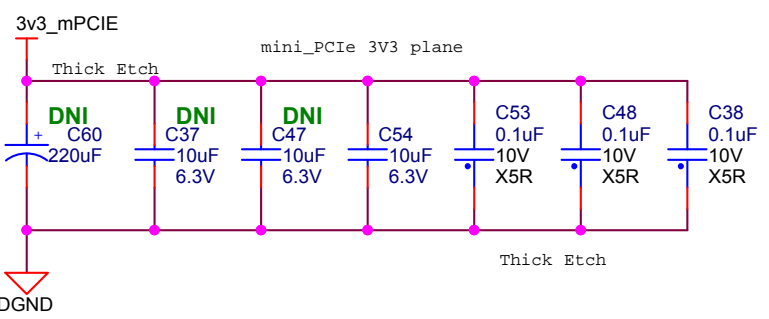
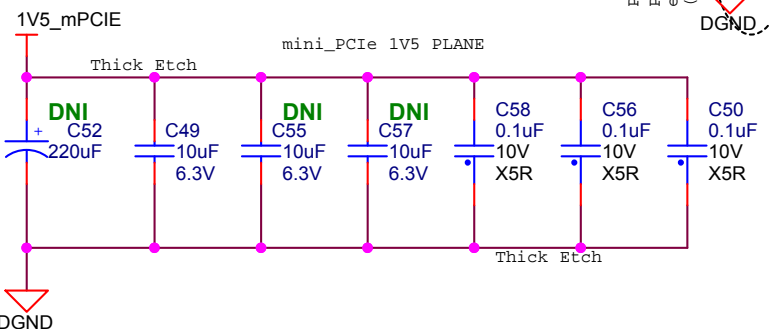
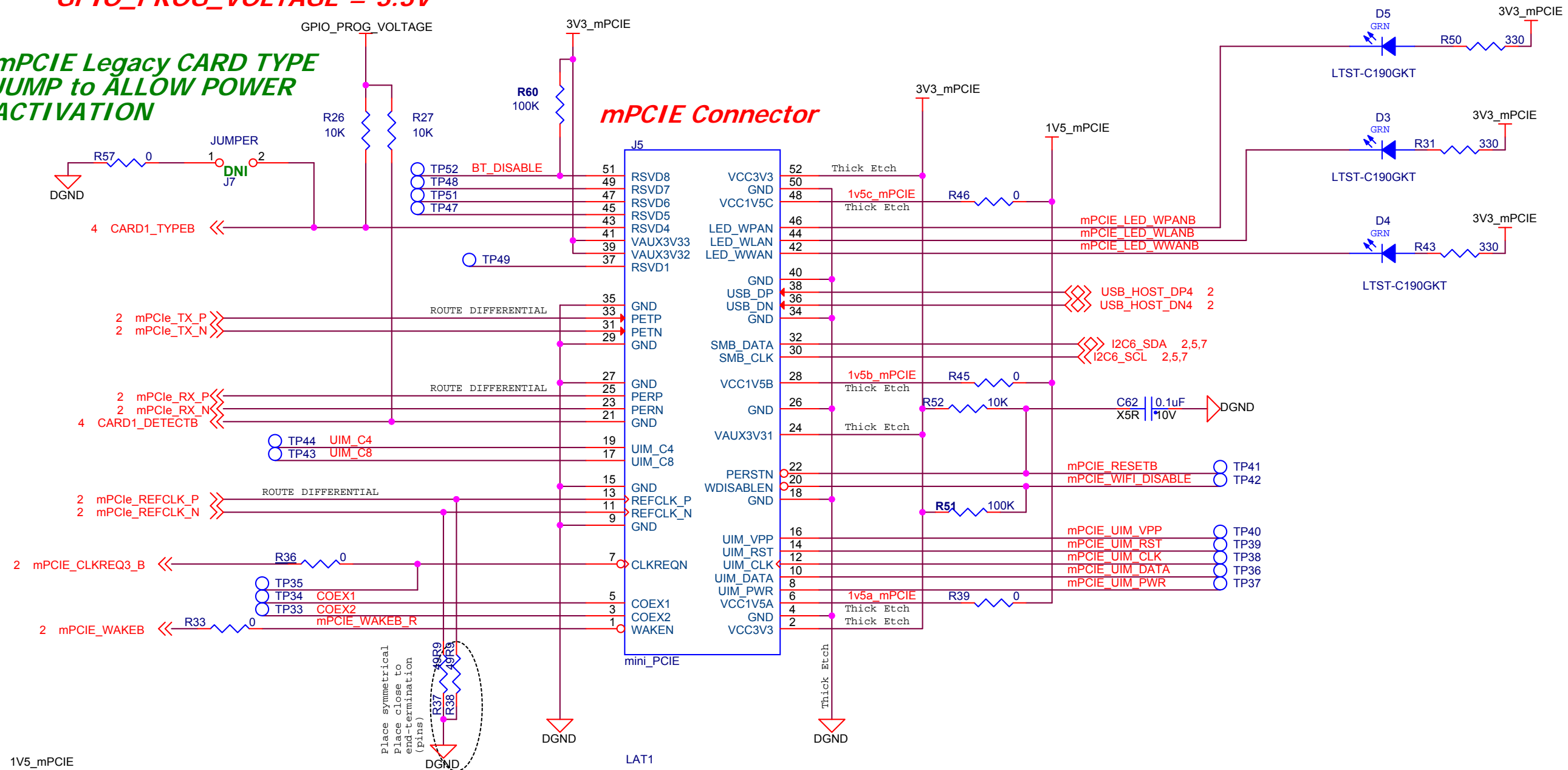


EXPANSION CONNECTOR

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GPIO_PROG_VOLTAGE = 3.3V

**mPCIe Legacy CARD TYPE
JUMP to ALLOW POWER
ACTIVATION**



Place symmetrical
Place close to
end-termination
(pins)

LAT1
mPCIe CONN LATCH

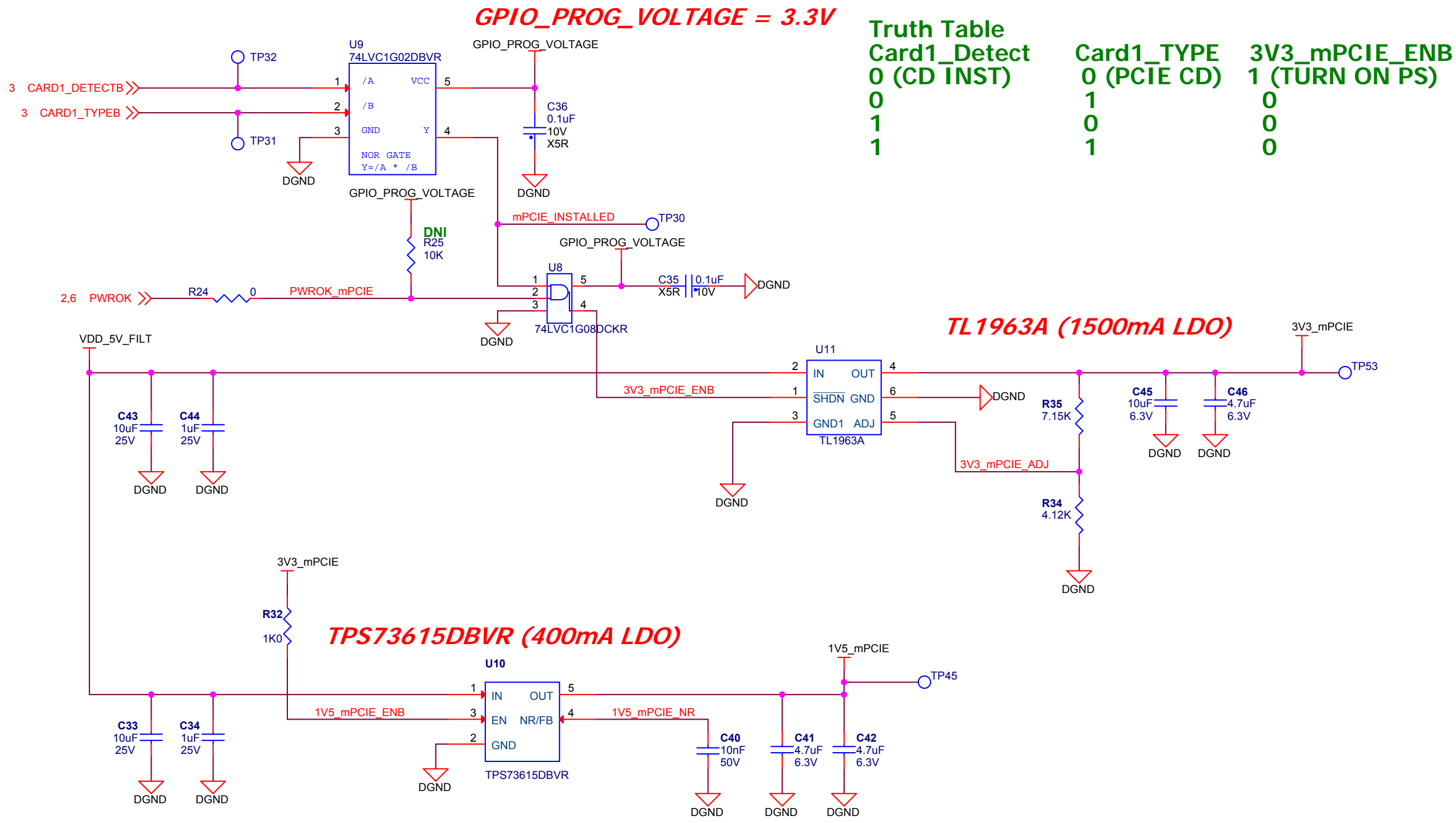
mPCIe SLOT



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GPIO_PROG_VOLTAGE = 3.3V



Truth Table

Card1_Detect	Card1_TYPE	3V3_mPCIE_ENB
0 (CD INST)	0 (PCIE CD)	1 (TURN ON PS)
0	1	0
1	0	0
1	1	0

TL1963A (1500mA LDO)

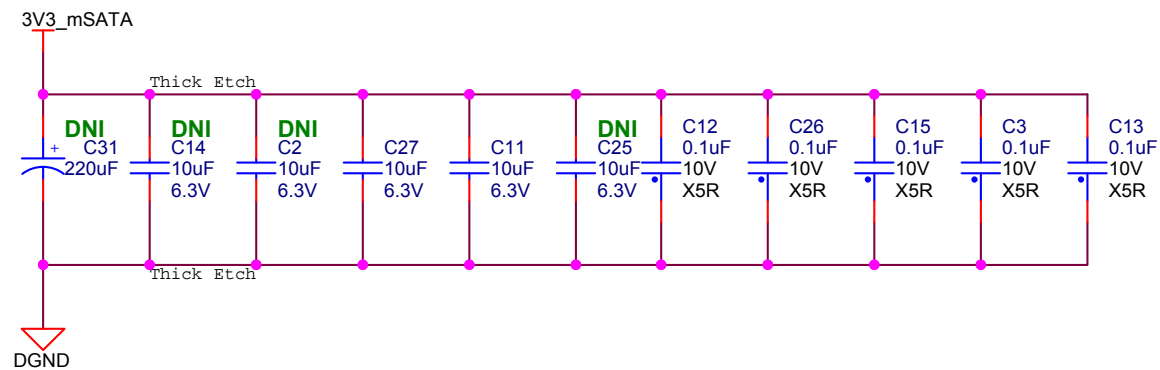
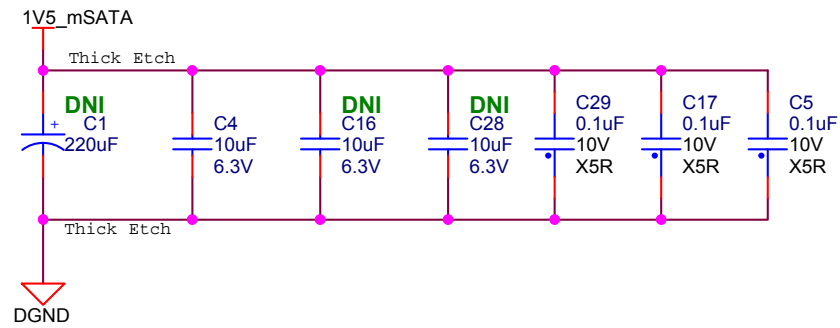
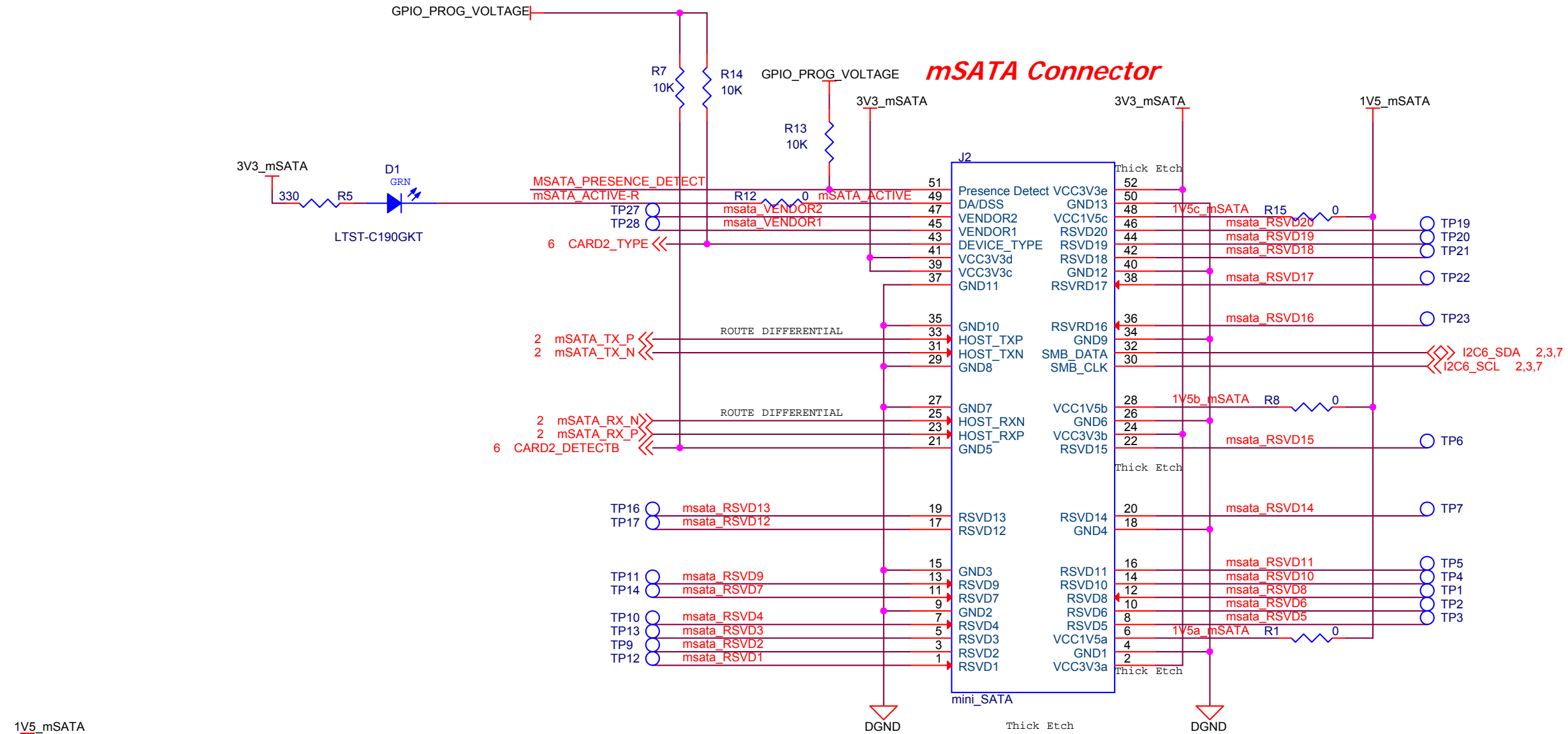
TPS73615DBVR (400mA LDO)

mPCIE POWER

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**MINNOWBOARD
3V3 PWR**

mSATA Connector

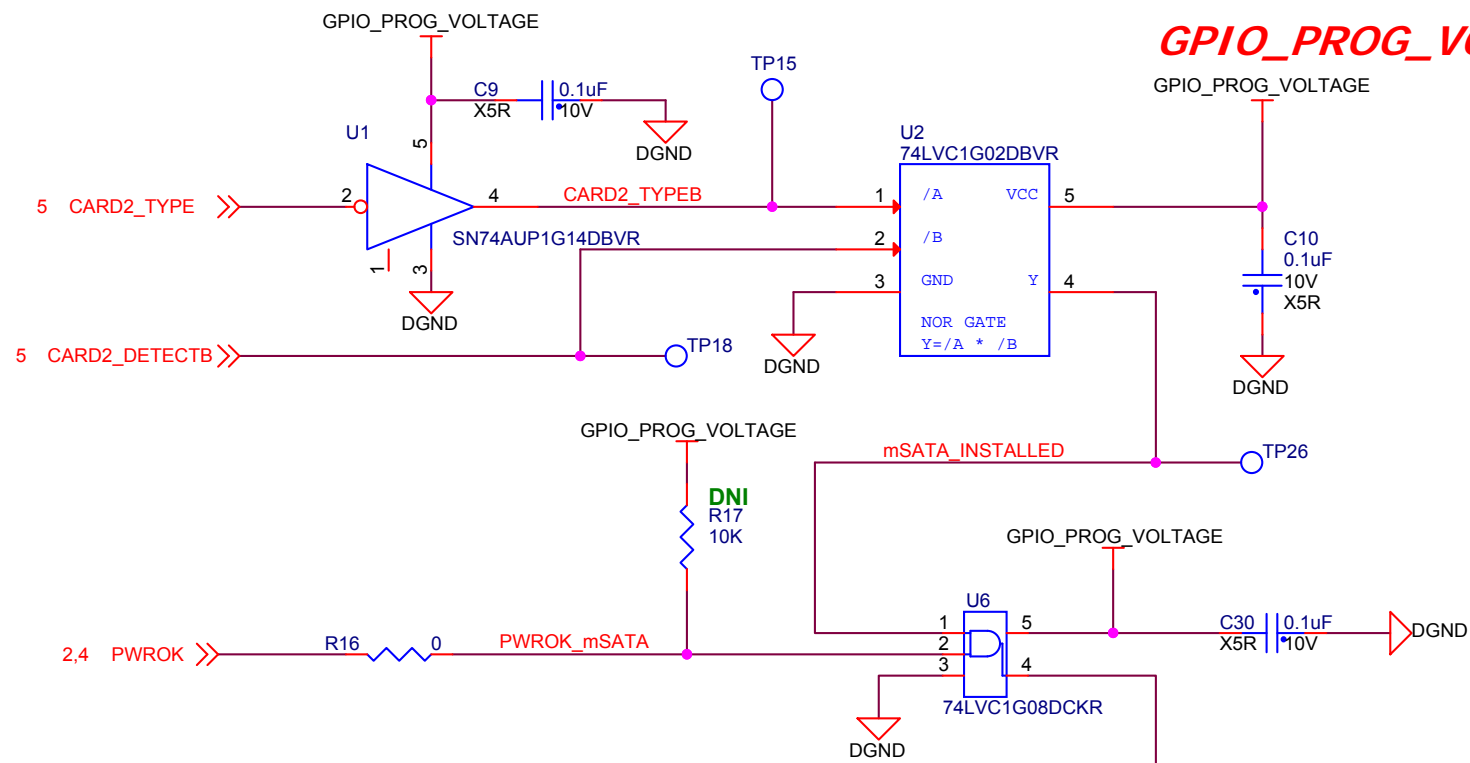


mSATA SLOT

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GPIO_PROG_VOLTAGE = 3.3V

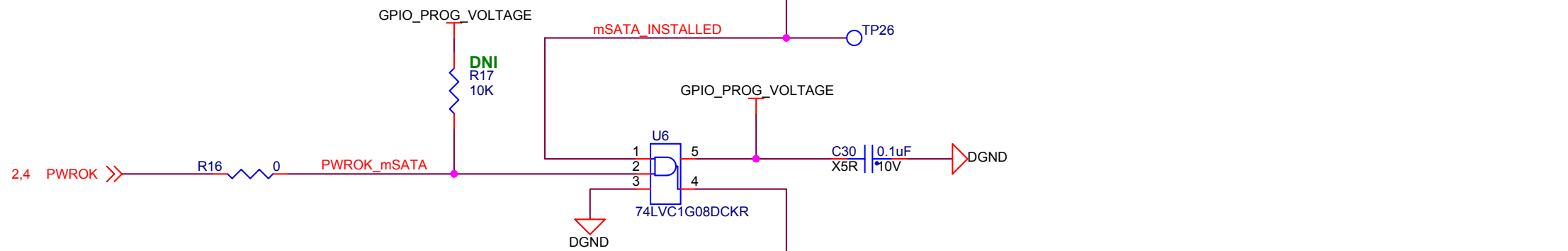
GPIO_PROG_VOLTAGE = 3.3V



Truth Table
Card2_Detect
0 (CD INST)
0
1
1

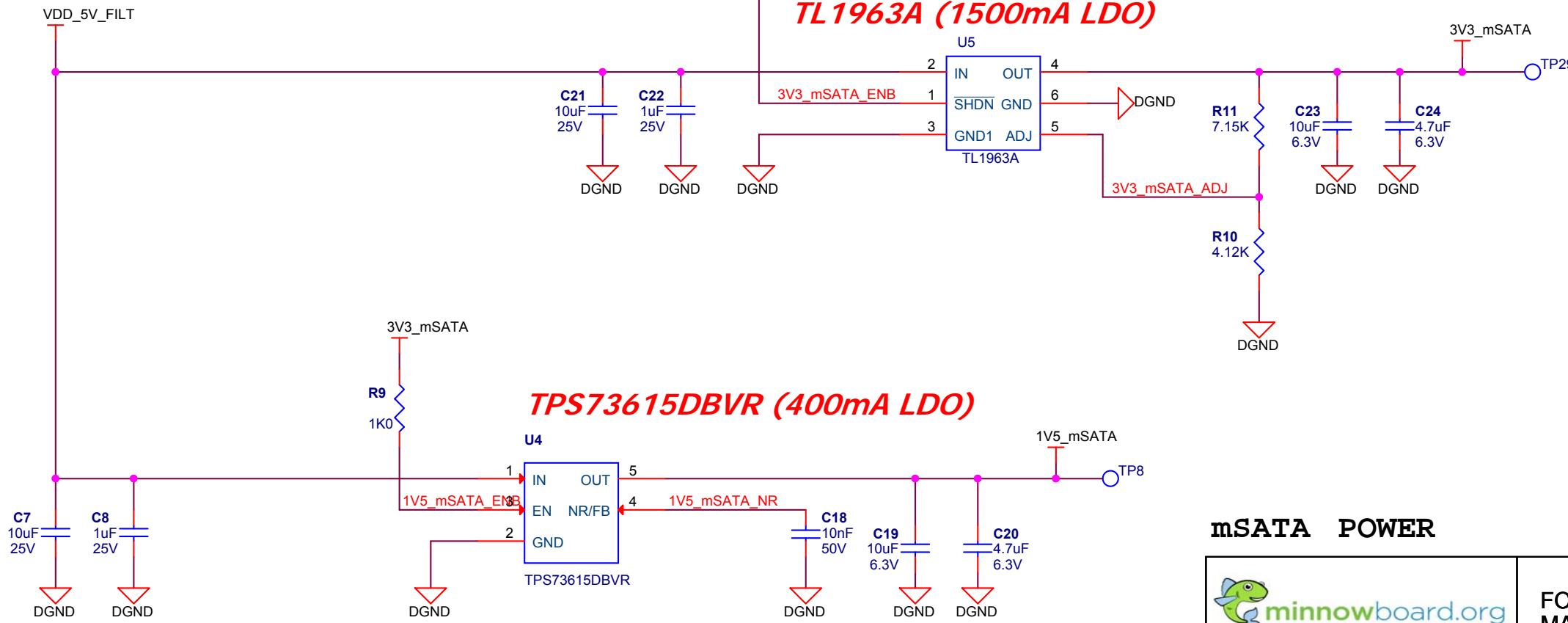
CARD2_TYPE
1 (CD MSATA)
0
1
0

3V3_mSATA_ENB
1 (PWR ON)
0
0
0



TL1963A (1500mA LDO)

TPS73615DBVR (400mA LDO)



mSATA POWER

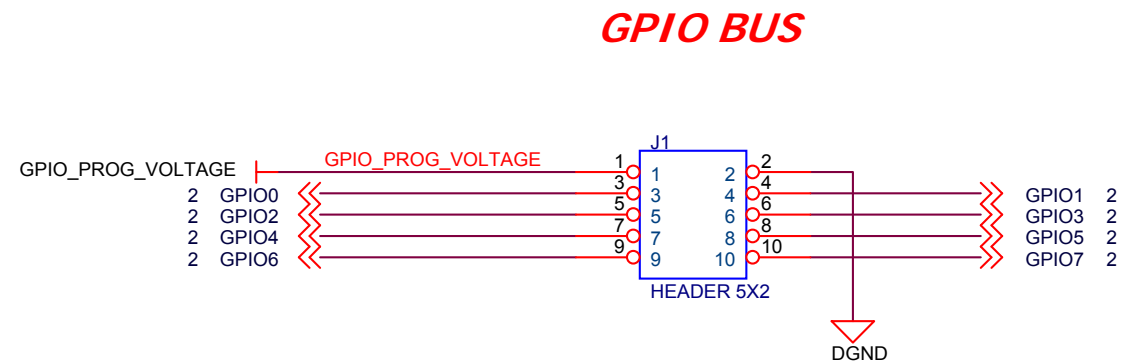
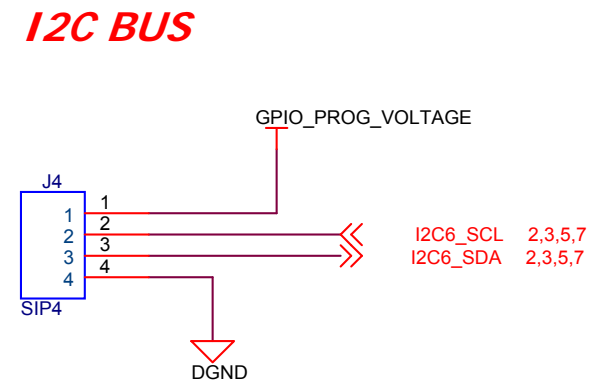
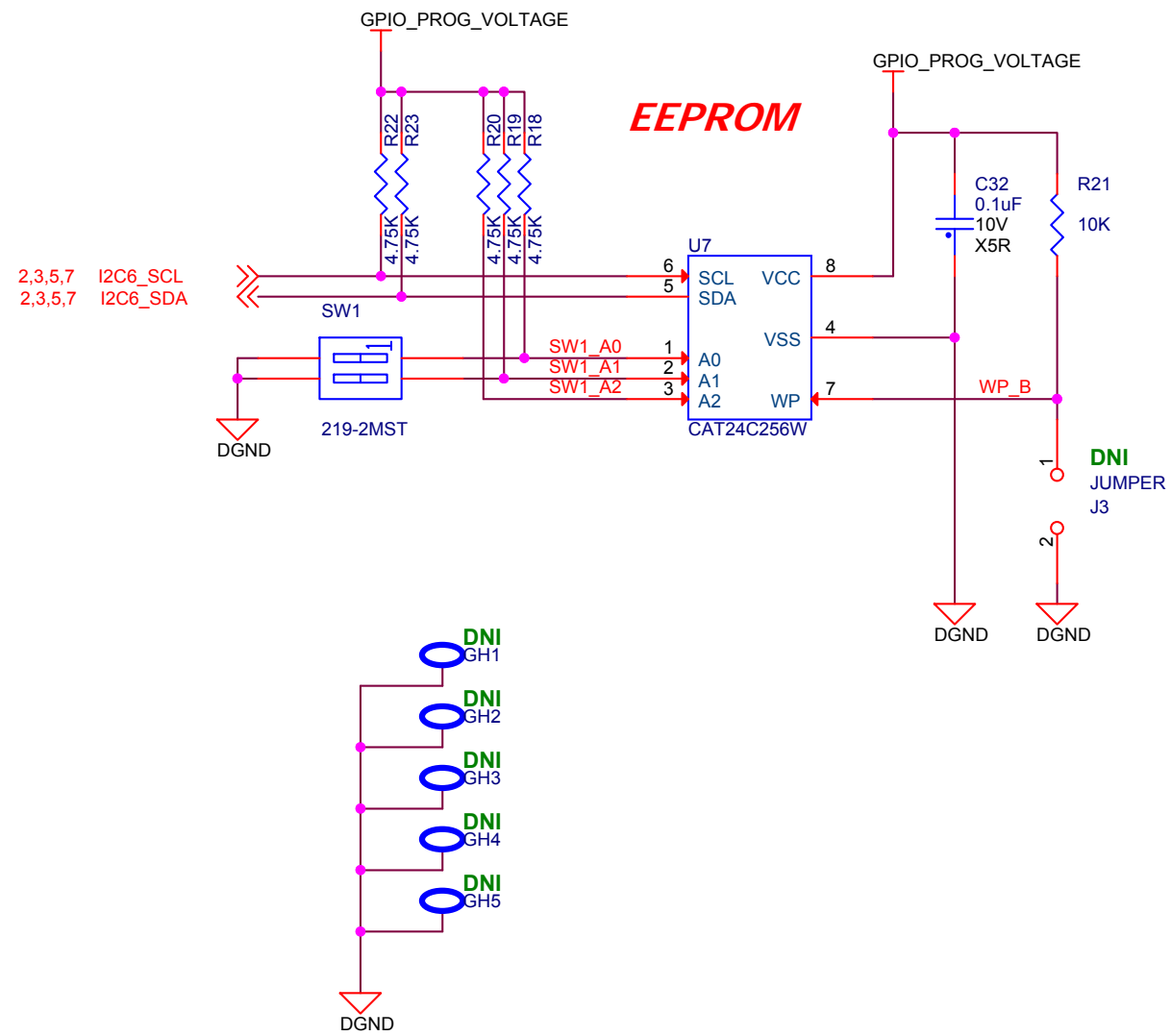


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ID EEPROM / I2C / GPIO

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